

NASA Electronic Parts and Packaging Program (NEPP)

2nd Annual Electronics Technology Workshop (ETW)

NASA GSFC, Building 3 Auditorium

Tuesday, June 28, 2011					
Start	Finish	Session	Topic	Presenter(s)	Organization
8:15	8:30	Overview	NEPP and the Goals of the Workshop, Logistics	Mike Sampson, and Kenneth LaBel	NASA/GSFC
8:30	9:00	System Issues - Complex Electronics	Class Y - NEPP Championed Approached to Advanced Package Qualification	Shri Agrawal	JPL
9:00	9:45	System Issues - Complex Electronics	FPGA - Overview of JPL Efforts under NEPP	Douglas Sheldon	JPL
9:45	10:15	System Issues - Complex Electronics	Discussion: FPGAs and new Alternatives	Kenneth LaBel	NASA/GSFC
10:15	10:45	Break			
10:45	11:45	System Issues - Complex Electronics	Taming the SEU Beast - Approaches and Results for FPGA Devices and How To Apply Them	Melanie Berg	MEI
11:45	13:15	Lunch			
13:15	14:15	System Issues - Complex Electronics	Status and Plans for Vanderbilt's SEU Tools and Research	Kevin Warren	Vanderbilt
14:15	14:45	Power	SEE Issue Update for Power MOSFETs	Jean Marie Lauenstein	NASA/GSFC
14:45	15:00	Break			
15:00	15:45	Power	NEPP POL Consortia Report	Philippe Adell, and Dakai Chen	JPL, NASA/GSFC
15:45	16:15	Power	Emerging Power Device Technologies and Radiation	Steve McClure	JPL
16:15	17:15	Power	Panel Session: Next Generation Power Devices and Systems	TBD	TBD
17:15		Discussion			

Wednesday, June 29, 2011					
Start	Finish	Session	Topic	Presenter(s)	Organization
8:30	9:15	Invited	An Update on the NRO/AFRL's National High Reliability Electronics Virtual Center (HiREV)	Chris Bozada	AFRL
9:15	9:30	Power	Commercial Vendor Experience with GaN and SiC	Alan Tasker	Microsemi
9:30	10:15	Mixed Signal	Reliability of SiGe, SOI, and Advanced Mixed Signal Devices for Cryogenic Space Missions	Richard Patterson	NASA/GRC
10:15	10:30	Break			

Wednesday, June 29, 2011 (Cont.)					
Start	Finish	Session	Topic	Presenter(s)	Organization
10:30	11:00	Memories	Challenges of Developing Qualification Methods for DDR class Memories - Part 1	Ray Ladbury	NASA/GSFC
11:00	11:30	Memories	Challenges of Developing Qualification Methods for DDR class Memories - Part 2	Steve Guertin	JPL
11:30	12:00	Invited	Counterfeit Electronics - NASA Update	Phil Zulueta	JPL
12:00	13:30	Lunch			
13:30	14:00	Systems on a Chip	Status on Radiation Qualification Methods for SOC	Steve Guertin	JPL
14:00	14:30	System Issues - Complex Electronics	Reliability Testing of Advanced Area Array Packages for Space	Reza Ghaffarian	JPL
14:30	15:00	System Issues - Complex Electronics	NEPP Program at MSFC - Advanced Packages and CAVE Consortia	Jim Blanche	Jacobs Technology
15:00	15:30	Break			
15:30	16:00	Passives	Capacitor Test, Evaluation, and Modeling within NEPP	Alexander Teverovsky	MEI Technologies
16:00	17:00	Passives	Panel: What's Next for Capacitors for Space?	Mike Sampson	NASA/GSFC
17:00		Discussion			

Thursday, June 30, 2011					
Start	Finish	Session	Topic	Presenter(s)	Organization
8:30	9:00	Invited	When is Qualification not Qualification?	Mike Sampson	NASA/GSFC
9:00	9:30	Advanced Electronics	Radiation Status on Sub-65nm Electronics	Jonathan Pellish	NASA/GSFC
9:30	10:00	Photonics	Status of Qualification Guidelines for Fiber Optic Components and Systems	Melanie Ott	NASA/GSFC
10:00	10:30	Photonics	Update on Cryogenic Latchup and Detectors	Cheryl Marshall	NASA/GSFC
10:30	11:00	Break			
11:00	11:30	Radiation Effects	SiGe Technology - Radiation Update	Paul Marshall	Consultant
11:30	12:00	Discussion	What Are We Missing?	Mike Sampson, and Ken LaBel	NASA/GSFC
12:00		End of workshop			