PERFORMANCE SPECIFICATION SHEET

CAPACITORS CHIP, MULTIPLE LAYER,
FIXED, CERAMIC DIELECTRIC, ESTABLISHED RELIABILITY
AND NON-ESTABLISHED RELIABILITY
STYLE CDR06

This specification is approved for use by all Departments
and Agencies of the Department of Defense.

The requirements for acquiring the product described herein shall consist of this
specification and MIL-PRF-55681.

<table>
<thead>
<tr>
<th>Dimensions</th>
<th>Inches</th>
<th>mm</th>
</tr>
</thead>
<tbody>
<tr>
<td>L ±.020</td>
<td>.010</td>
<td>0.25</td>
</tr>
<tr>
<td></td>
<td>.015</td>
<td>0.38</td>
</tr>
<tr>
<td></td>
<td>.020</td>
<td>0.51</td>
</tr>
<tr>
<td></td>
<td>.025</td>
<td>0.64</td>
</tr>
<tr>
<td></td>
<td>.080</td>
<td>2.03</td>
</tr>
<tr>
<td></td>
<td>.225</td>
<td>5.72</td>
</tr>
<tr>
<td></td>
<td>.250</td>
<td>6.35</td>
</tr>
</tbody>
</table>

NOTES:
1. Dimensions are in inches.
2. Metric equivalents are given for information only.
3. Dimensions and tolerances are for bare chips. For solder coated terminations, add .025 inch (0.64
   mm) to the positive length tolerance and .015 inch (0.38 mm) to the positive width and thickness
tolerances.

FIGURE 1. Dimensions and configuration.

AMSC N/A
DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

FSC 5910
REQUIREMENTS:

Dimensions and configuration: See figure 1.

Capacitance value: See table I.

Capacitance tolerance: \( J = \pm 5 \) percent, \( K = \pm 10 \) percent, \( M = \pm 20 \) percent. See table I.

Rated voltage: See table I.

Product level: Non-established Reliability - C; Established Reliability - M, P, R, S.

Operating temperature range: \(-55^\circ C \) to \(+125^\circ C\).

Marking: In accordance with MIL-PRF-55681.

### TABLE I. Capacitor characteristics.

<table>
<thead>
<tr>
<th>Part or Identifying Number (PIN) 1/</th>
<th>Capacitance (pF)</th>
<th>Capacitance tolerance</th>
<th>Rated temperature and voltage temperature limits</th>
<th>Rated voltage (V dc)</th>
</tr>
</thead>
<tbody>
<tr>
<td>CDR06BP682B - - -</td>
<td>6,800</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BP822B - - -</td>
<td>8,200</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BP103B - - -</td>
<td>10,000</td>
<td>J, K</td>
<td>BP</td>
<td>100</td>
</tr>
<tr>
<td>CDR06BX394AK - - -</td>
<td>390,000</td>
<td>K</td>
<td>BX</td>
<td>50</td>
</tr>
<tr>
<td>CDR06BX474A - - -</td>
<td>470,000</td>
<td>K, M</td>
<td>BX</td>
<td>50</td>
</tr>
</tbody>
</table>

1/ Complete PIN shall include additional symbols to indicate capacitance tolerance (where applicable), termination finish, and product level.

Custodians:
Army - CR
Navy - EC
Air Force - 85

Review activities:
Navy - AS, MC, OS, SH
Air Force - 19, 99
DLA - CC

Preparing activity:
Army - CR

Agent:
DLA - CC

(Project 5910-1923-04)