

2019 NASA NEPP Electronics Technology Working Group Meeting  
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NASA Goddard

# AS6294/1 and AS6294/3 – PEMs and PEDs for Space

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# AS6294 PEM Task Team

Celebrating over 30 years of providing high quality semiconductor development and test services.

## 2 Balloted Documents - AS6294/1 & /3

- Joint CE-12 / JC-14.3 PED / PEM Flows
  - AS6294/1 - Requirements for Plastic Encapsulated **Microcircuits** in Space Applications - Released
  - AS6294/3 - Requirements for Plastic Encapsulated **Discrete Semiconductors** in Space Applications – In Process of Being Released

# SAE CE-12 PEM Task Team

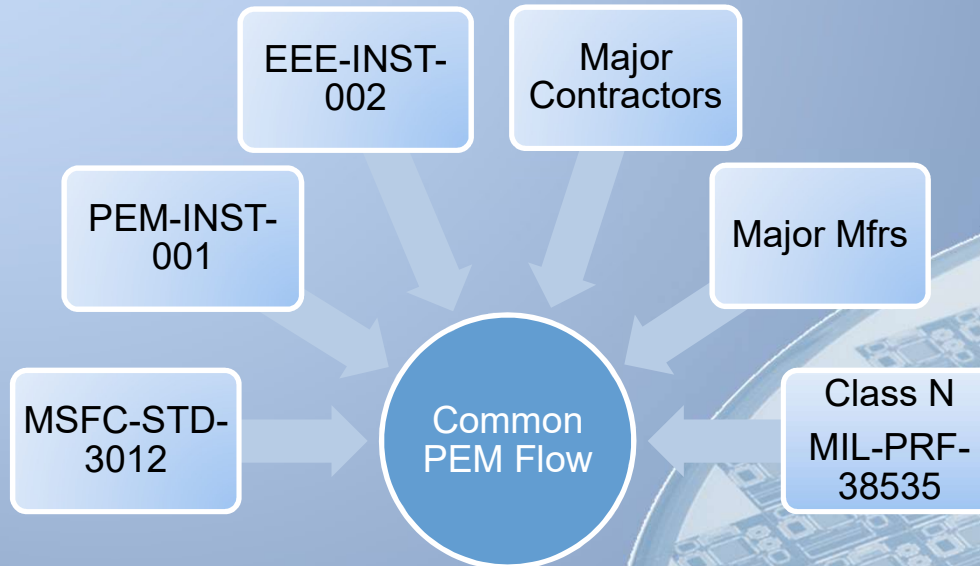
- Leadership
  - Anduin Tow – CE12 Chair
  - Sultan Ali Lilani - Integra Technologies (PEM Task Team Chair)
  - David Locker – US Army (Team Lead – Avionics / Terrestrial PEMs)
  - Rod de Leon – Boeing Satellite (Team Lead – Space PEMs)
- Meetings: Weekly (started 2/25/2014)
  - Alternate weekly meetings between Space and Avionics / Terrestrial
- Members
  - On average, 15 members attend weekly meetings (65+ members in distribution list)
  - Representatives from Boeing, Lockheed, NG, L-3, Aerospace, NASA, Xilinx, ON, TI, ADI, Intel, Army, Air Force, Honeywell, IRF, Rockwell Collins , DLA, BAE, Integra Technologies, Hi-Rel Lab. DPACI etc.

- **What Motivated Us to Start the Task Team**
  - PEMs being used for Avionics / Terrestrial and in some Space applications
    - Automotive grade parts are getting wider acceptance
    - Commercial parts are being used after screening and qualification
    - In some cases; Industrial grade parts are being used with no screening and qualification
  - Very little usage of Class N devices for Space application
  - Some usage of EP (Enhanced Plastic) devices
  - No one comprehensive flow existed that OEMs can use either for A/T or Space application. PEM-INST-001 or Marshall MSFC-STD-3012 s extensively used by many OEMs but frequently modified by OEMs for their application
  - There are many other flows that various OEMs use and significantly differ from PEM-INST-001
  - PEM-INST-001 and other flows are written around Microcircuits
    - However; we see Discrete devices also for used in plastic packages

# Task team focus

*Objective is to come up with a Standardized PEMs Flow for the Industry*

*Comparison of known Industry PEMs flow and Major Subcontractors, OEM, OCM practices*



*Consensus was to use Marshall MSFC-STD-3012 as baseline and cross referenced Goddard PEM-INST-001, MIL Handbook & Standards, JEDEC-Standards documents*

A large, detailed image of a microcircuit chip, showing a dense grid of square dies on a circular substrate. The chip is the central focus of the slide, with a blue gradient background.

## Leveraging Existing Standards

- JEDEC
- AEC
- Mil Stds

### Examples:

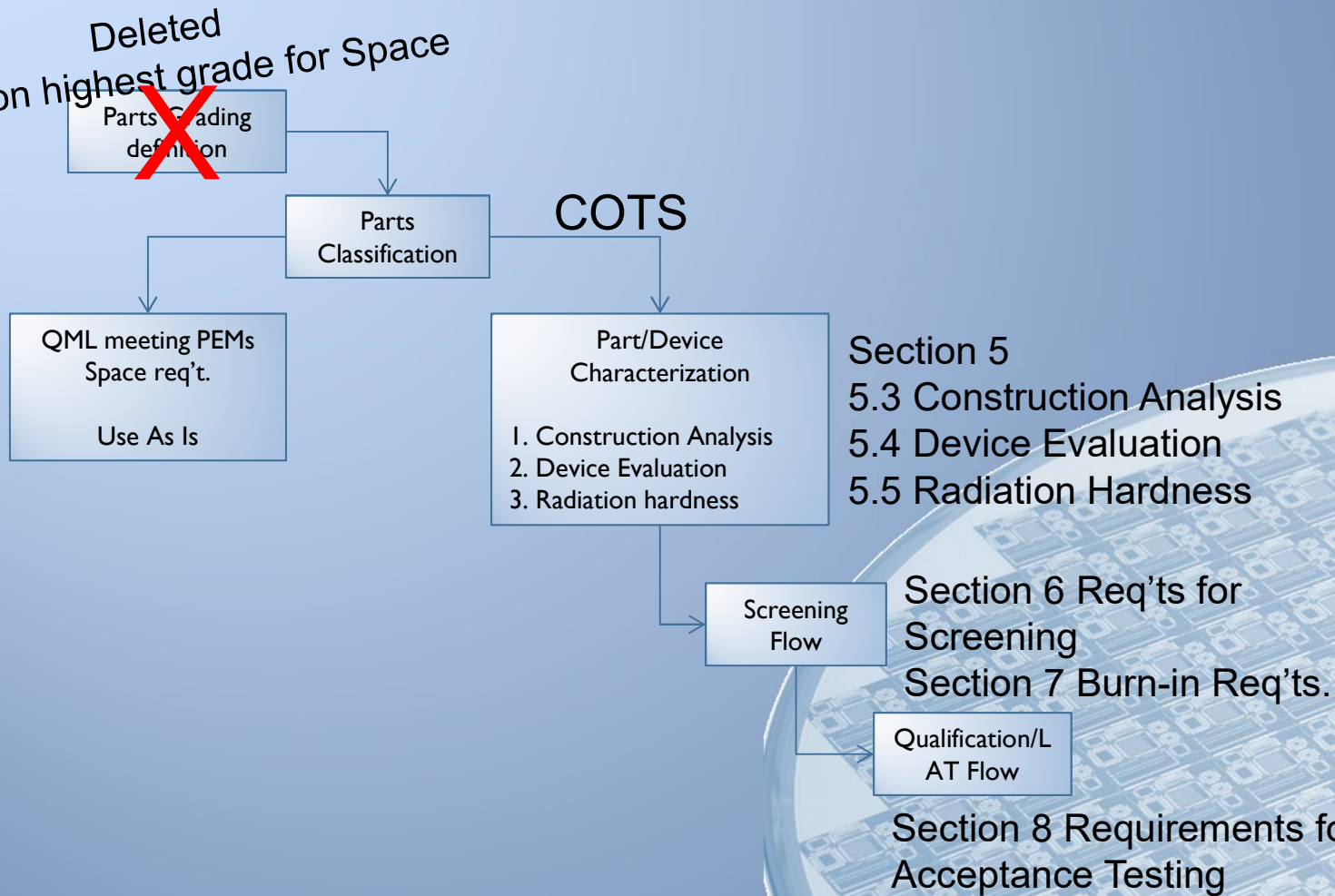
- Copper Bond Wire
- Radiation Testing
- SAM
- Temp Cycle

# AS6294/1 Microcircuits

Celebrating over 30 years of providing high quality semiconductor development and test services.

# Document foundation

Deleted  
Focus on highest grade for Space





# Document section overview

Parts  
Classification

Section 4  
Device Classification  
determination

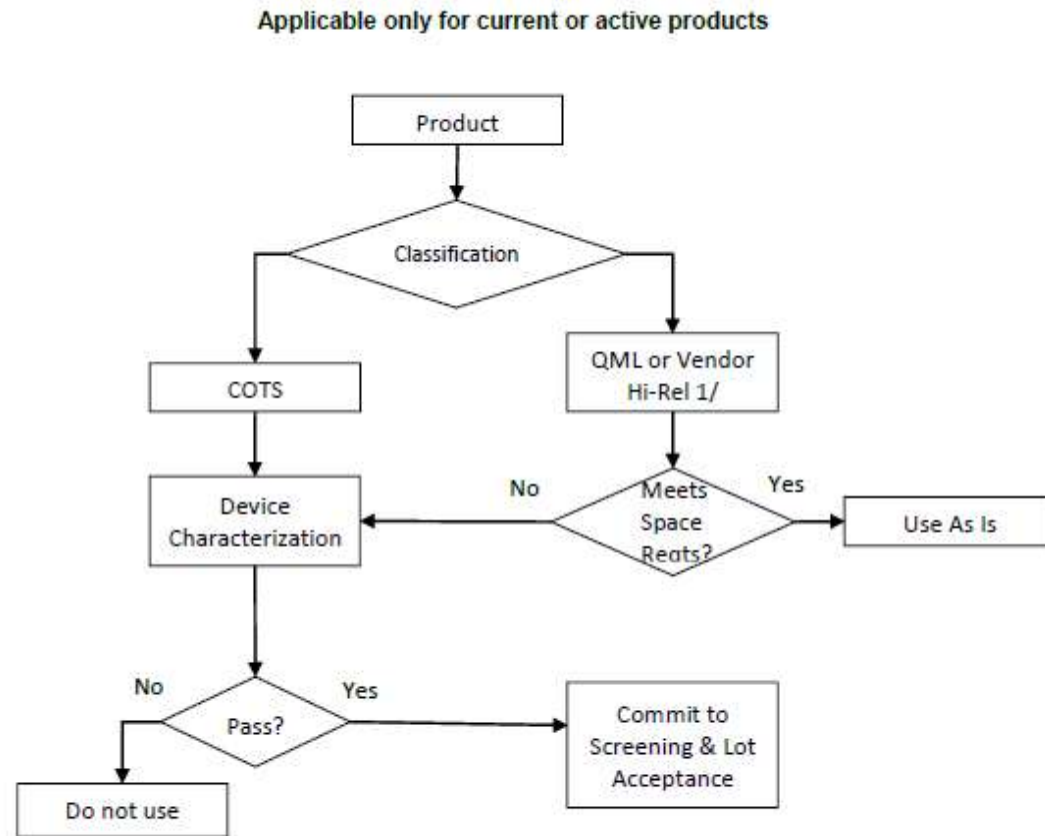


Figure 1 - Device classification determination  
1/ - QML Class N is non-space, VID (vendor item drawing), EP (enhanced product)

# Document section overview

## Part/Device Characterization

1. Construction Analysis
2. Device Evaluation
3. Radiation Hardness

## Section 5 Device Characterization

### 5.3 Construction Analysis

### 5.4 Device Evaluation

### 5.5 Radiation Hardness

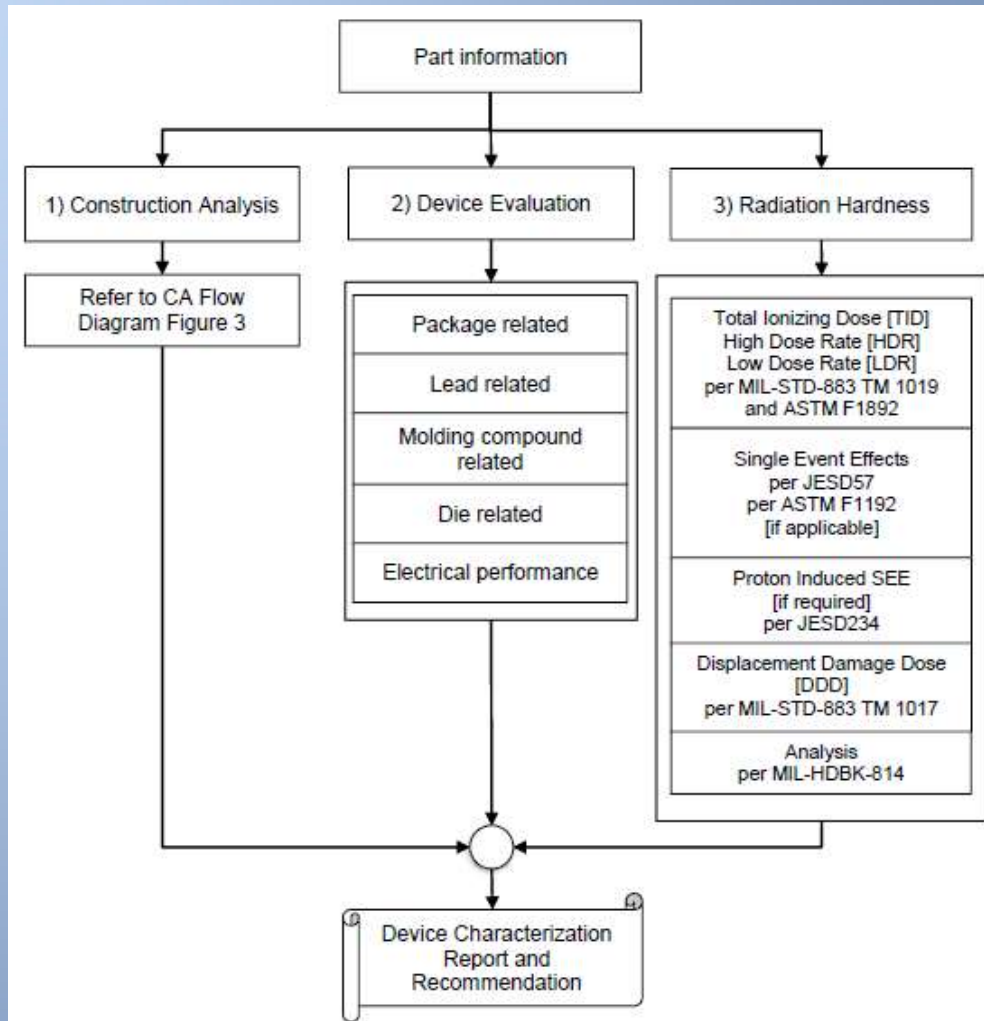


Figure 2 - Typical device characterization flow for PEMS

**Table 1 - Manufacturer information**

Enhanced from PEM-INST-001

#	Category	Information/Question
1		Part number
2		Function
3	General Information	Date code
4		Package type
5		Manufacturer
6		Device datasheet
7		Die process technology
8		Die revision or identification
9		Die layout or geometric configuration
10		Wafer lot information and fabrication location
11		ESD sensitivity level per Human body model (HBM) and Charged device model (CDM).
12		Moisture sensitivity level
13	Part	Date of last die revision
14		Date of introduction to the market
15		Product storing policy (years to keep in stock)
16		Packing parts for shipment, moisture control
17		Type of molding compound and characteristics (glass transition temperature, CTE, flame retardant)
18	Manufacturer	Vendor facility (location)
19		Point of contact for quality assurance
20		Quality certification of the vendor (ISO 9000 or equivalent)
21		Mask revision control
22		Application support
23		Part traceability
24		Availability of Statistical Process Control (SPC) data
25		What kind of 100% outgoing inspection and screening is used?
26		Availability of test flowchart
27	Process	Availability of reliability and quality assurance data
28		Average outgoing quality (AOQ) <u>1</u> /
29		Major process capability indexes for the part (Cpk) <u>2</u> /
30		Acceptable proportion of failures at high temperature measurements
31		Radiation hardness of the process or of similar parts
32		Are there any military parts manufactured using same technology?

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Part/Device Characterization

1. **Construction Analysis**
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Section 5 Device Characterization

5.3 Construction Analysis

5.4 Device Evaluation

5.5 Radiation Hardness

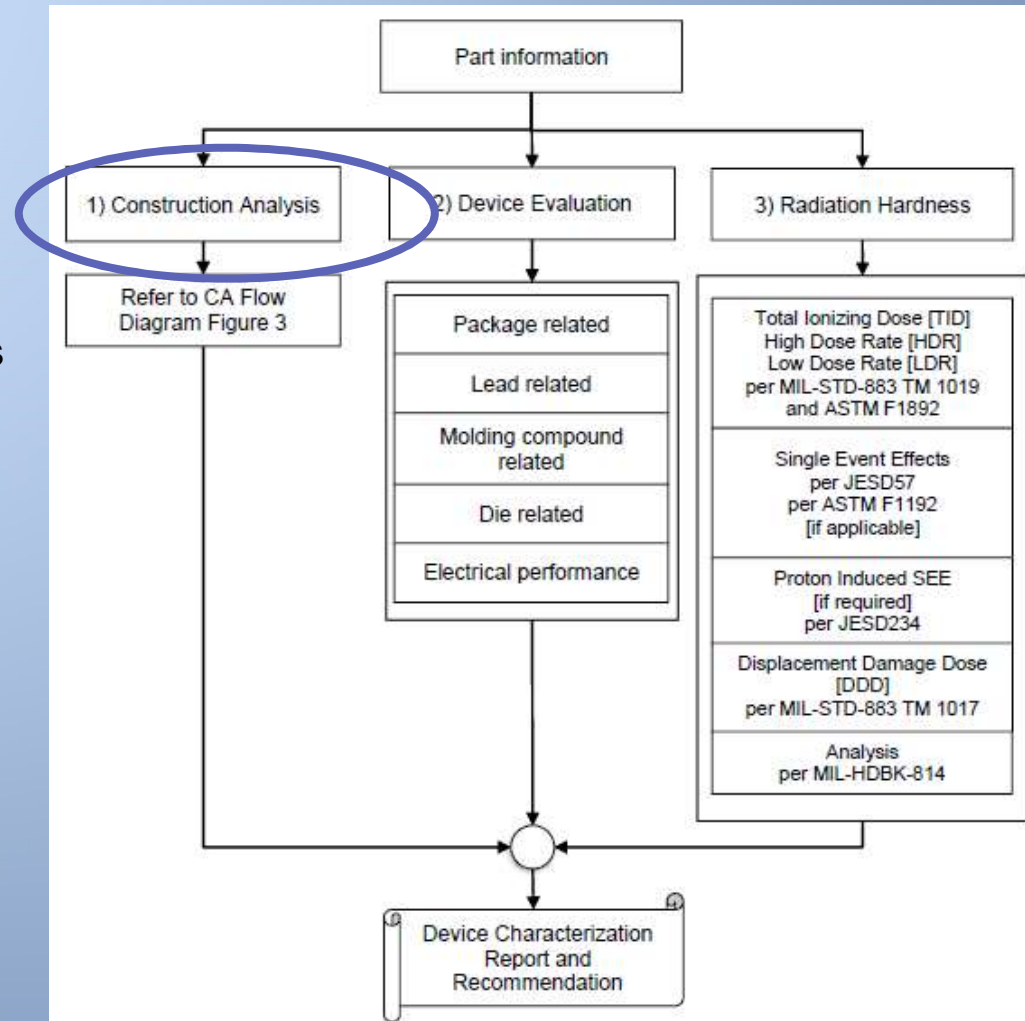
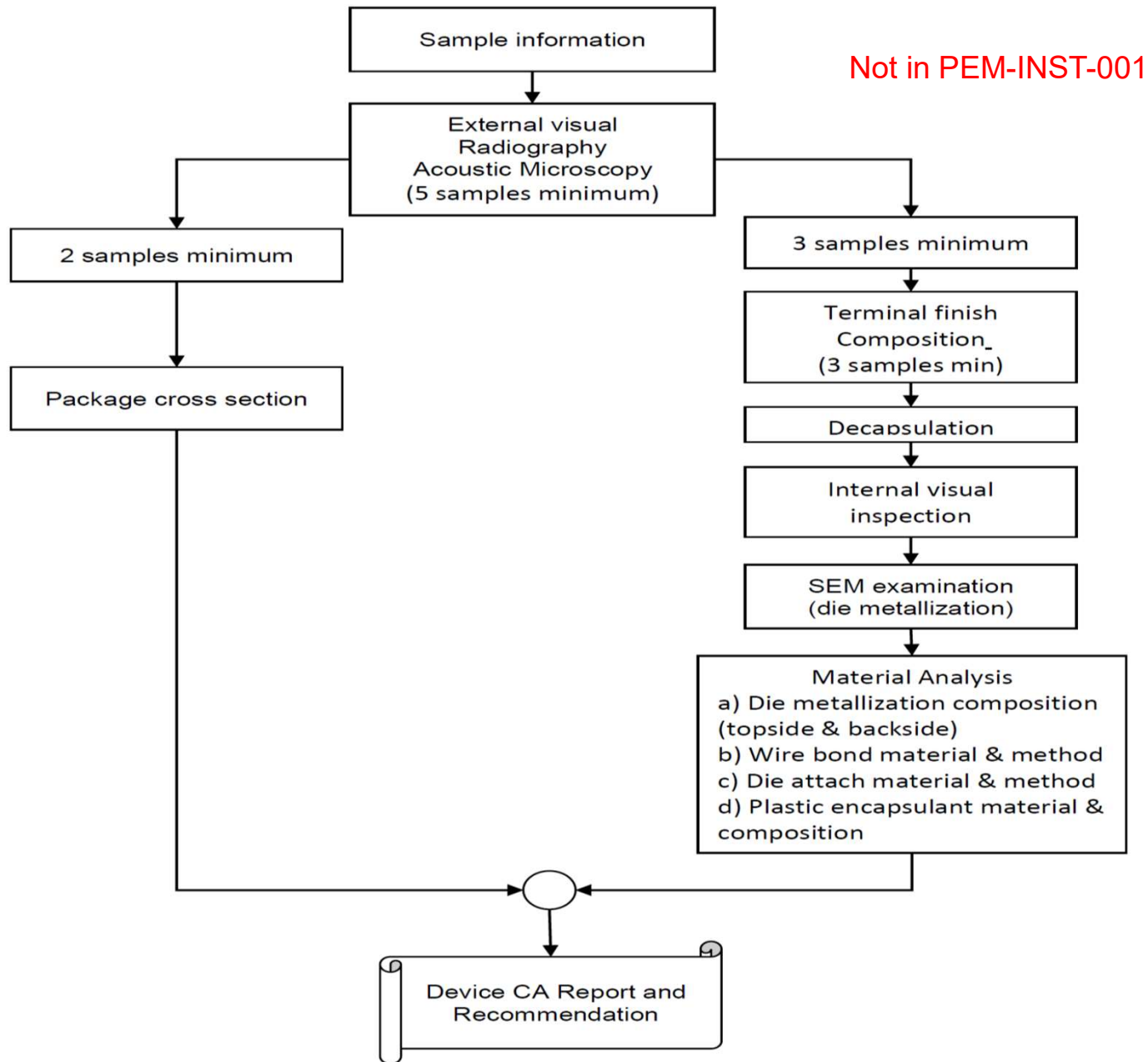


Figure 2 - Typical device characterization flow for PEMS



**Figure 3 - Typical construction analysis test flow for PEMs**

# Document section overview

Part/Device  
Characterization

1. Construction Analysis
2. **Device Evaluation**
3. Radiation Hardness

Section 5 Device  
Characterization

5.3 Construction Analysis

5.4 Device Evaluation

5.5 Radiation Hardness

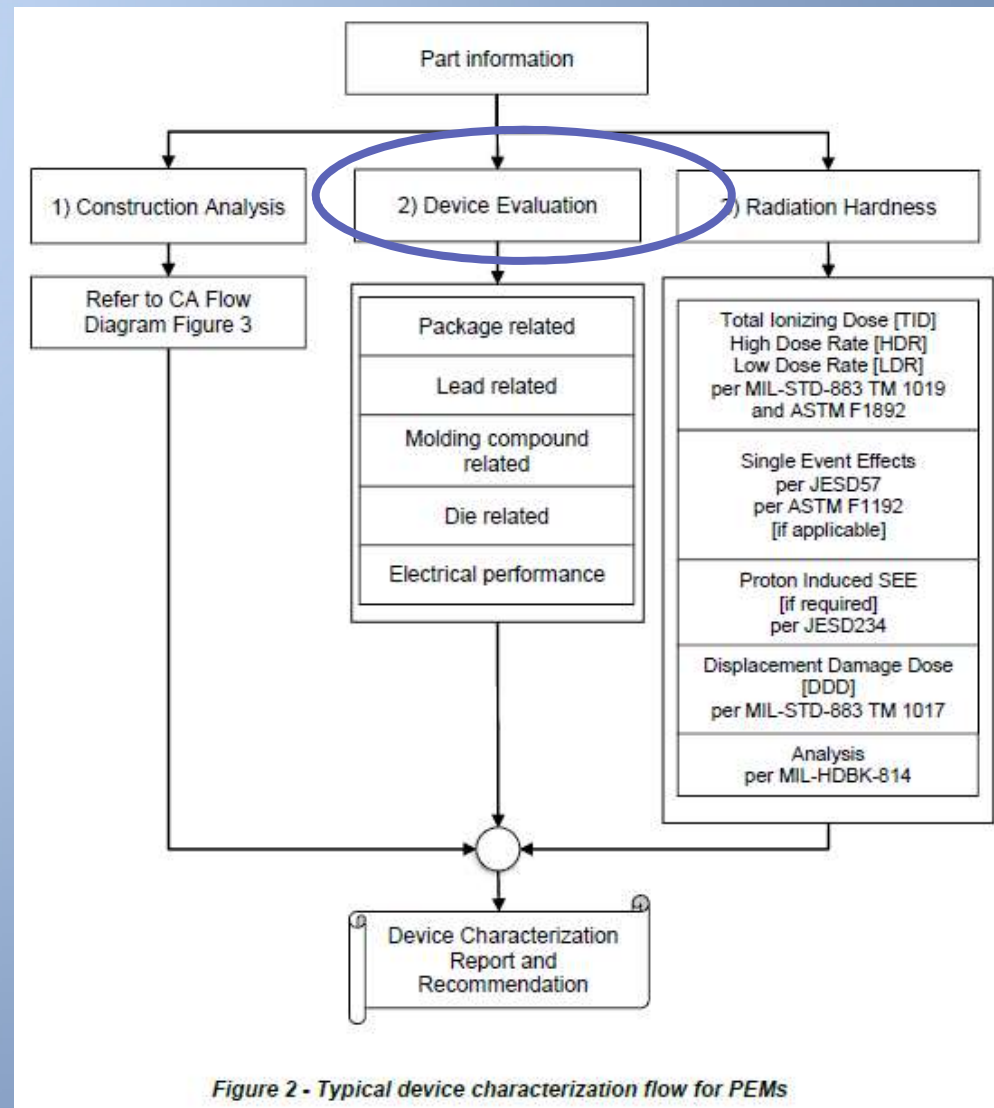


Figure 2 - Typical device characterization flow for PEMs

# Document section overview

## Part/Device Characterization

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## Section 5 Device Characterization

### 5.3 Construction Analysis

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#### 5.4.2 Recommended Evaluations

Significantly Enhanced Info than PEM-INST-001

In some cases, to address special quality or reliability concerns, an extended set of examinations to characterize design and materials used in PEMs may be required. The list of characteristics in Table 3 below gives an example of data that can be required.

Table 3 - Recommended evaluations or tests

1. Package-related	Physical dimensions, weight.
2. Terminal-related characterization	Solderability
	Terminal finishing materials (addressing tin whiskers problems)
	Mechanical integrity of leads
3. Molding compound-related characterization	Outgassing
	Mechanical characteristics (glass transition temperature [T <sub>g</sub> ], coefficient of thermal expansion [CTE])
	Chemical characteristics (impurities [P, Cl, Br, Na])
	α-particle emission
	Types of flame retardant
	Moisture characteristics (moisture diffusion and hygroscopic expansion coefficients); verify moisture sensitivity level (MSL) classification
4. Die-related characterization (materials and design)	Passivation
	Interlayer dielectric system
	Metallization system such as thickness, composition, EM rules, Antenna rules
5. Electrical performance characterization	Electrical at ambient, minimum, maximum temperature ranges (datasheet and the application specification range limits) to establish margins for utilization in Worst Case Analysis

# Document section overview

## Part/Device Characterization

1. Construction Analysis
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3. **Radiation Hardness**

## Section 5 Device Characterization

### 5.3 Construction Analysis

### 5.4 Device Evaluation

### 5.5 Radiation Hardness

Radiation evaluation shall address all threats appropriate for the technology, application, and environment, including total ionizing dose (TID), Enhanced Low Dose Rate Sensitivity (ELDRS), single event effects (SEE), and displacement damage degradation (DDD) as defined in the project ionizing radiation control document.

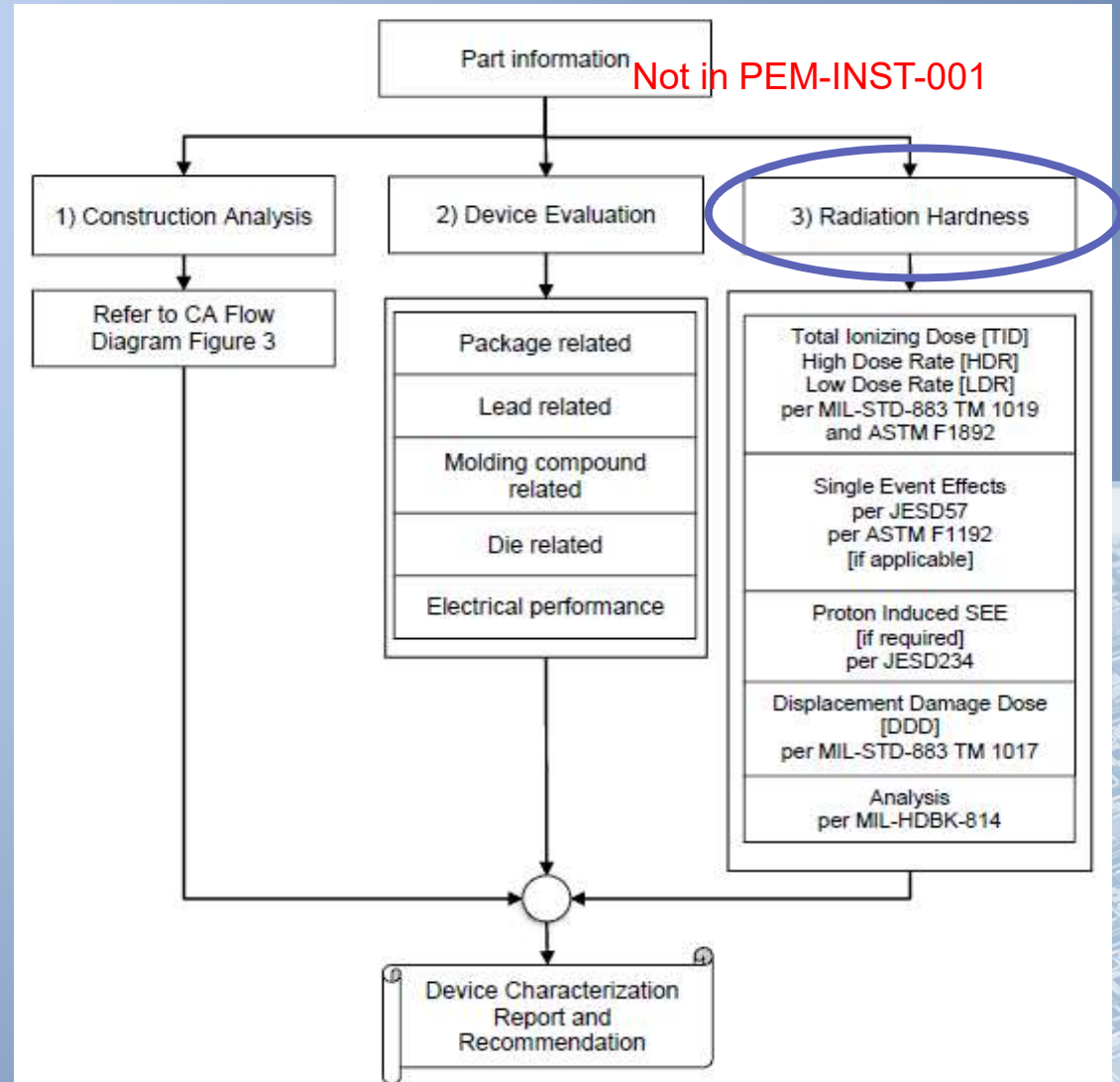


Figure 2 - Typical device characterization flow for PEMs



# Document section overview

## Screening Flow

Section 6 Req'ts for Screening

Section 7 Burn-in req'ts.

Section 7 Burn-in req'ts. In accordance with JEP163 as applicable

### 6. REQUIREMENTS FOR SCREENING

- a. Screening is applied to all flight parts in each lot by testing and inspecting every sample, and proactively affects reliability of the lot. Typically, the date code defines a lot and if additional information is available then that should be used for lot differentiation (e.g., wafer lot identification, part lot number). If there is no date code available the user has to contact the OEM/manufacturer to identify the lot.
- b. Refer to Table 4 for screening requirements of PEMs. A typical test flow for screening of PEMs is shown in Figure 4.

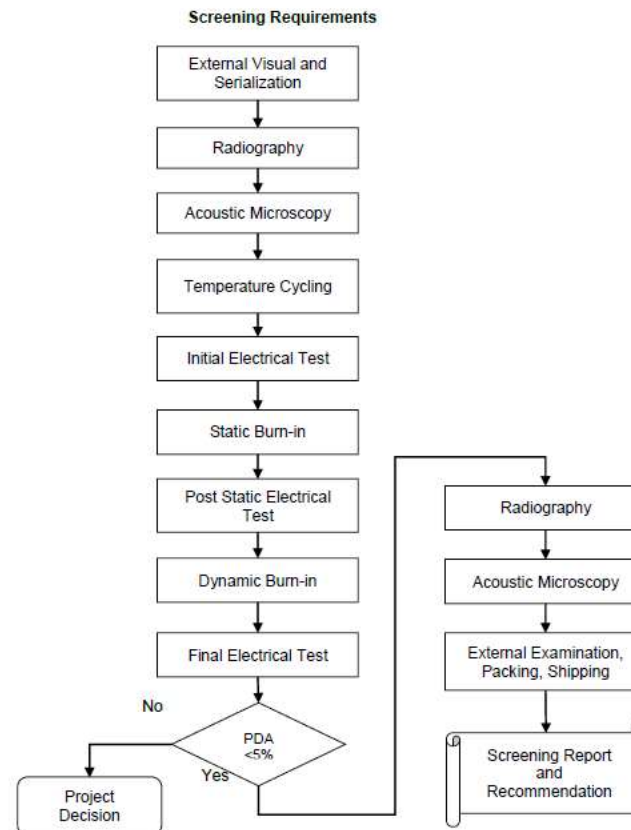


Figure 4 - Typical screening test flow for PEMs

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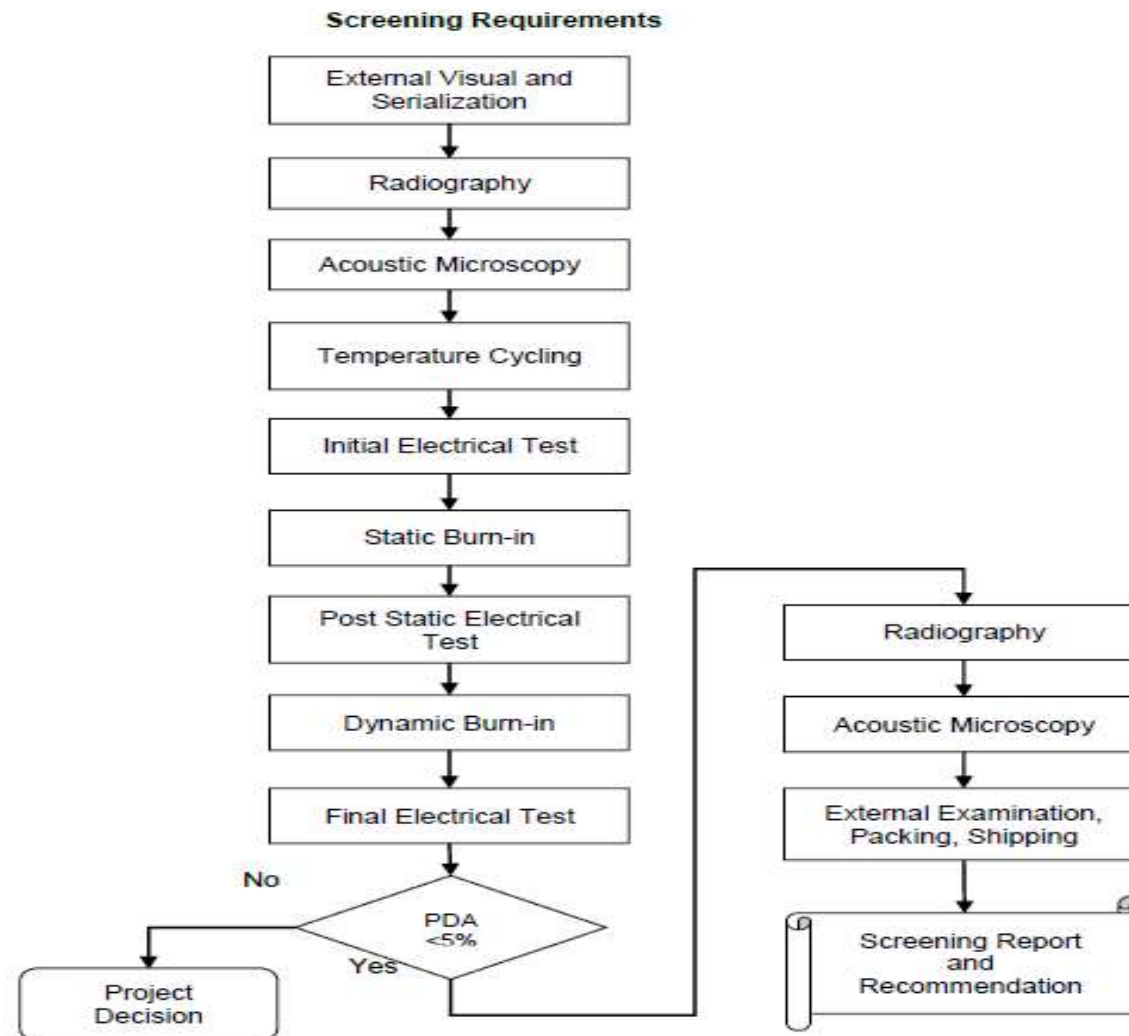
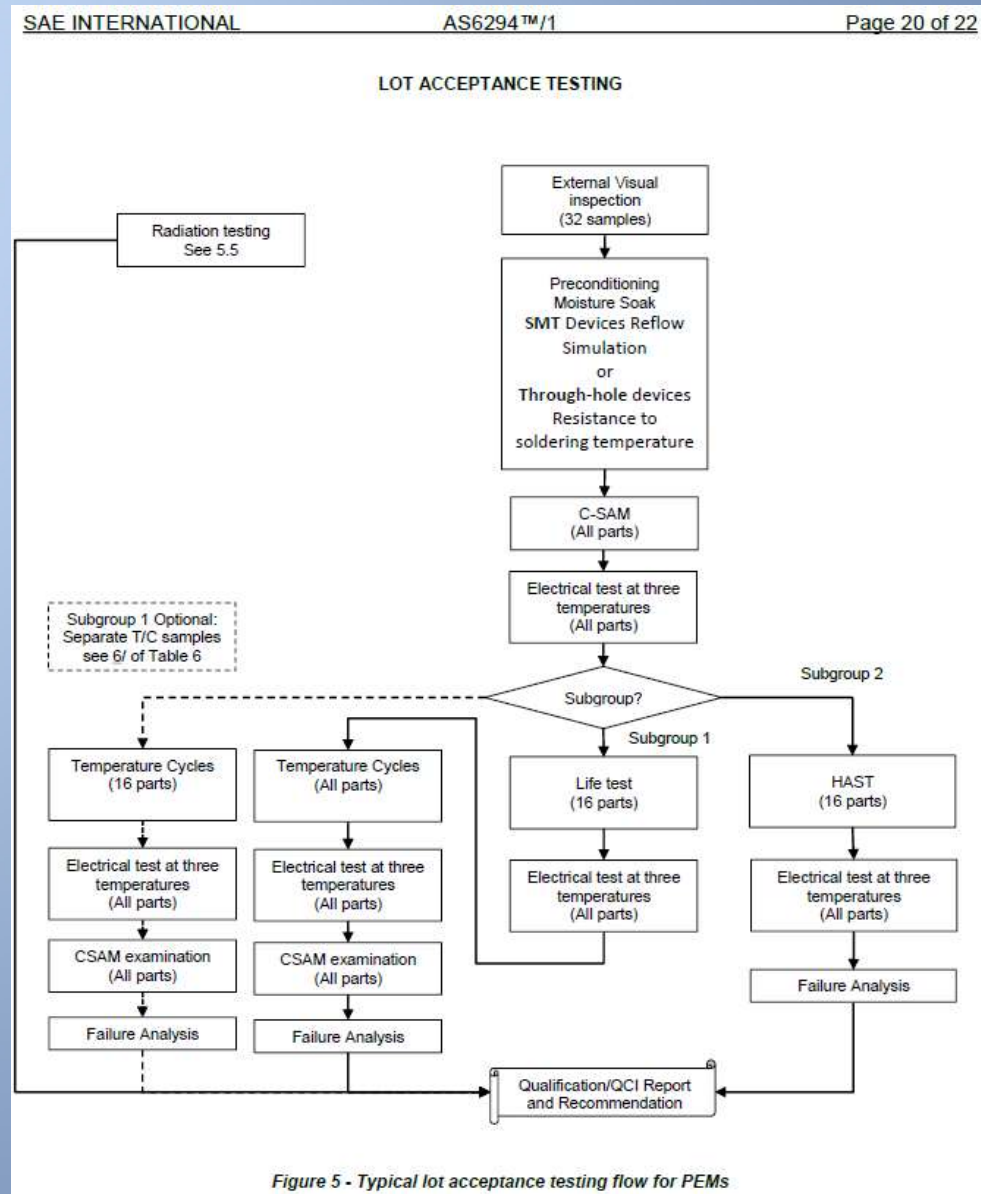


Figure 4 - Typical screening test flow for PEMs

# Document section overview

## Qualification/LAT Flow

### Section 8 Requirements for Lot Acceptance Testing



## LOT ACCEPTANCE TESTING

Sample size is different  
then PEM-INST-001

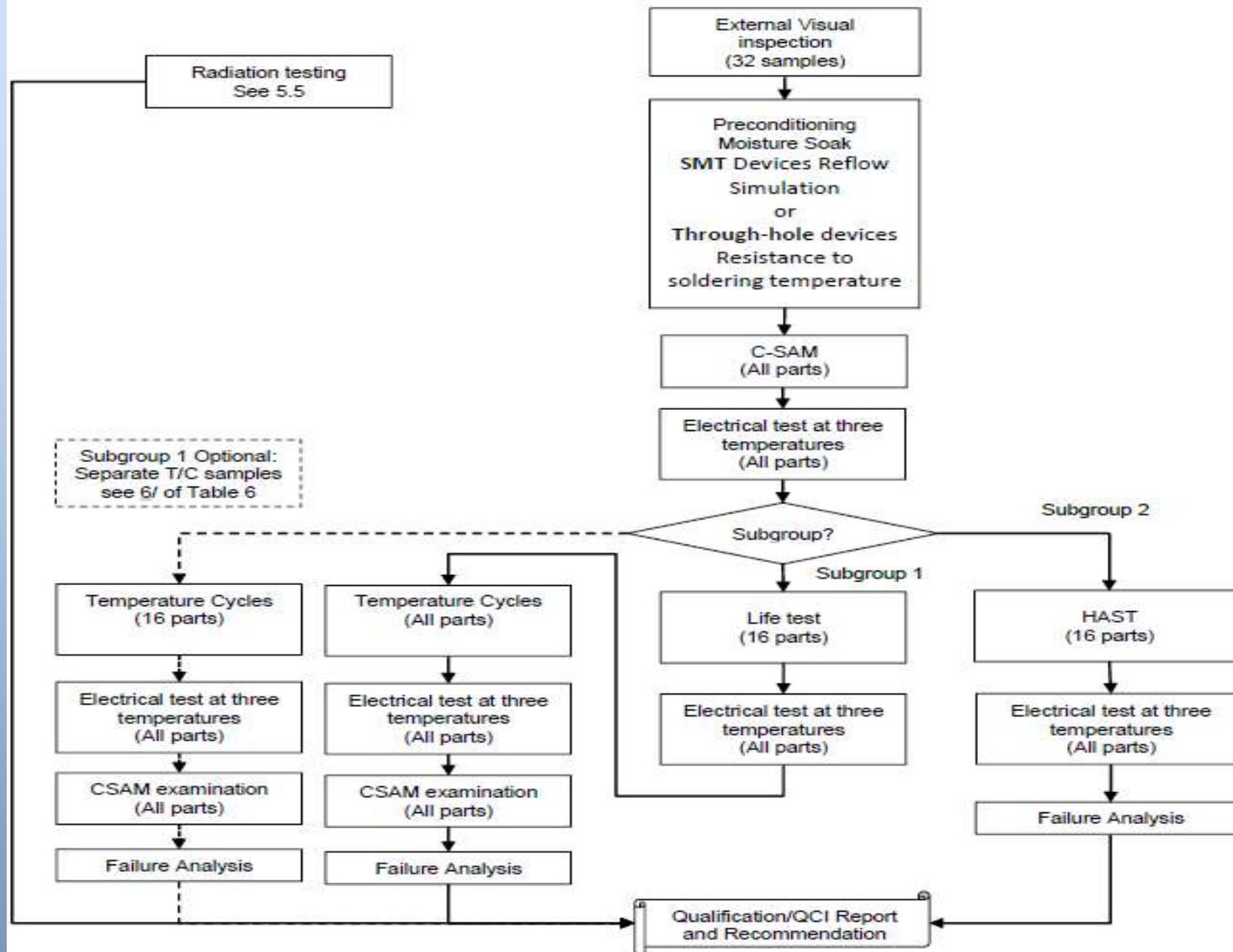


Figure 5 - Typical lot acceptance testing flow for PEMs

# Adaptation by Manufacturers

- Analog Devices (CSL, CSM, CSH) – Very similar flow for CSH)
- TI Space EP (Not exactly but has some of the tests
- Renesas / Intersil (Very similar flow)



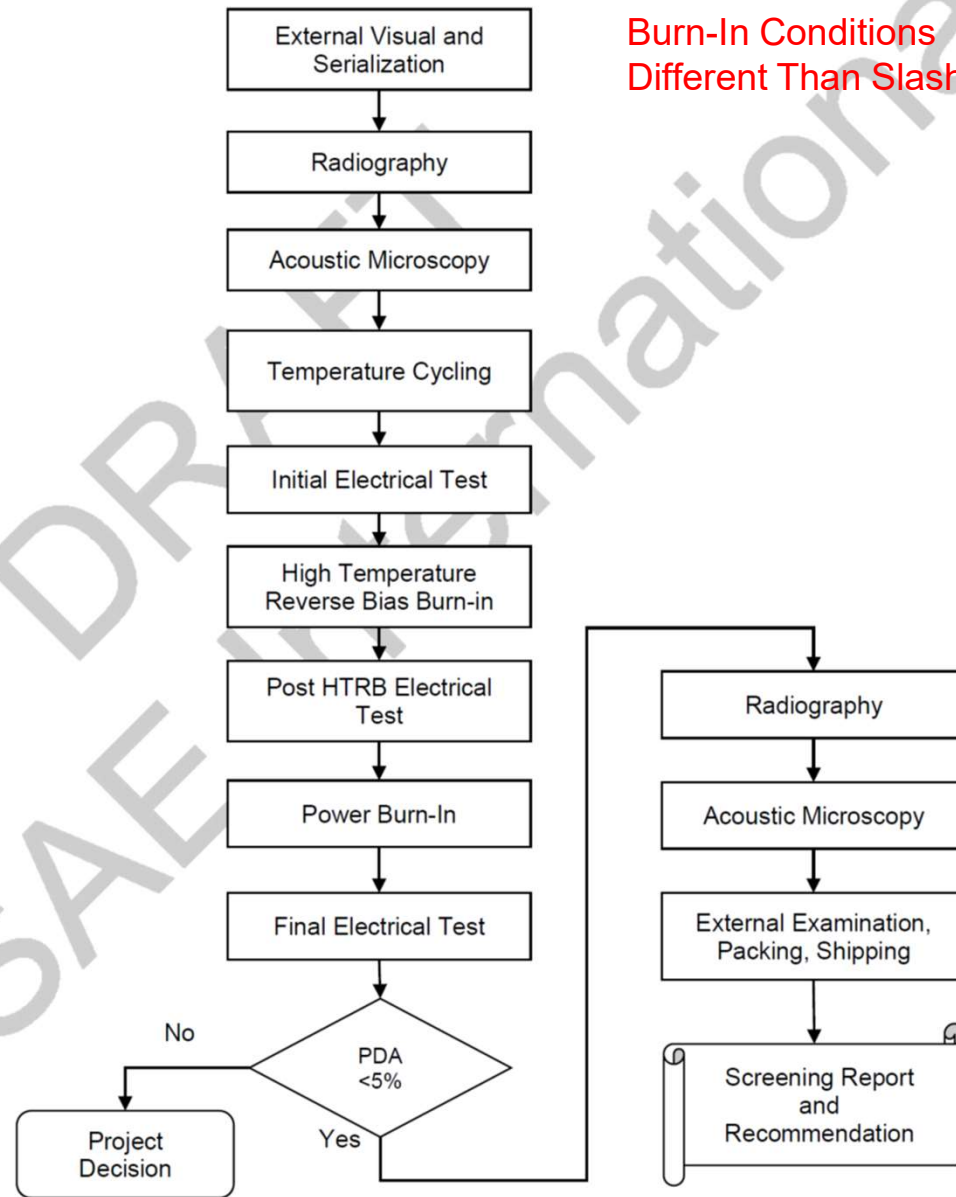
## **AS6294/3 PEDs**

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# Key Features - AS6294/3

- First Discrete PEM Document
- Leveraging AS6294/1 and EEE-INST-002
- Closely Aligned to (Work In Progress) MIL-PRF-19500 Appendix J (Discrete plastic package) (Benny Damron Leading)
- Major Differences to Slash 1
  - Burn-in and Life Test Conditions per Mil Std 750 and EEE-INST-002

SCREENING REQUIREMENTS



Burn-In Conditions  
Different Than Slash 1

Figure 4 - Typical screening test flow for PEDS



**Table 6 - Burn-in and electrical measurement requirements for transistors 1/, 2/, 3/**

Transistor Types	Required Burn-in		Delta Parameters	Electrical Measurements
	HTRB (Condition A)	Power (Condition B)		
Bipolar Transistors (Switching, Low High Power, Dual, General Purpose)	80% rated $V_{CB0}$ $125\text{ }^{\circ}\text{C} < T_A < 150\text{ }^{\circ}\text{C}$	Specify $V_{CB}$ or $V_{CE}$ to meet max $P_T$ $T_A = 25\text{ }^{\circ}\text{C}$	$\Delta I_{CB0}$ or $\Delta I_{CE0}$ $\Delta h_{FE}$	$I_{CB}$ , $I_{CE0}$ , $I_{CB0}$ , $I_{EBO}$ , $V_{(BR)CE0}$ , $V_{(BR)CB0}$ , $V_{(BR)EBO}$ , $V_{(BR)CES}$ , $V_{CE(SAT)}$ , $V_{BE(SAT)}$ , $h_{FE}$ , $t_{on}$ , $t_{off}$ , $t_s$ , $t_f$ , $h_{fe}$ , $C_{obo}$ , $C_{ibo}$
Bipolar Transistors (RF, High-Frequency)	80% rated $V_{CB0}$ $125\text{ }^{\circ}\text{C} < T_A < 150\text{ }^{\circ}\text{C}$	Specify $V_{CB}$ to meet max $P_T$ $T_A = 25\text{ }^{\circ}\text{C}$	$\Delta I_{CE0}$ $\Delta h_{FE}$	$I_{CE0}$ , $V_{(BR)CE0}$ , $V_{(BR)CB0}$ , $V_{(BR)EBO}$ $V_{CE(SAT)}$ , $h_{FE}$ $G_{PE}$ , $N_F$ , $h_{fe}$ , $\eta$ , $C_{obo}$
Junction Field Effect (JFET)	80% rated $V_{GS}$ $V_{DS} = 0$ $125\text{ }^{\circ}\text{C} < T_A < 150\text{ }^{\circ}\text{C}$	80% rated $V_{GS}$ Specify $V_{DS}$ to meet max $P_T$ $T_A = 25\text{ }^{\circ}\text{C}$	$\Delta I_{DSS}$ or $\Delta I_{GSS}$ $\Delta y_{fs}$	$V_{DS(ON)}$ , $V_{GS(OFF)}$ , $V_{(BR)GSS}$ , $I_{GSS}$ , $I_{DSS}$ , $C_{iss}$ , $C_{rss}$ , $y_{fs}$ , $y_{os}$
MOSFET	80% rated $V_{DS}$ $V_{GS} = 0\text{ V}$ $T_A = 125\text{ }^{\circ}\text{C}$	80% of rated $V_{GS}$ $V_{DS} = 0\text{ V}$ $T_A = 125\text{ }^{\circ}\text{C}$	$\Delta I_{DSS}$ or $\Delta I_{GSS}$ $\Delta V_{GS(TH)}$ $\Delta r_{ds(on)}$	$V_{(BR)DSS}$ , $V_{GS(TH)}$ , $V_{DS(ON)}$ , $V_{SD}$ , $r_{ds(on)}$ , $t_{on}$ , $t_{off}$ , $t_{rr}$ , $C_T$
Darlington	80% rated $V_{CB0}$ $125\text{ }^{\circ}\text{C} < T_A < 150\text{ }^{\circ}\text{C}$	Specify $V_{CB}$ or $V_{CE}$ to meet max $P_T$ $T_A = 25\text{ }^{\circ}\text{C}$	$\Delta h_{FE}$ $\Delta I_{CE}$	$V_{CE(SAT)}$ , $V_{BE(SAT)}$ , $V_{BE(TH)}$ , $V_{(BR)CE0}$ , $I_{CE0}$ , $I_{EBO}$ , $I_{CE}$ $h_{FE}$ , $t_{on}$ , $t_{off}$ , $C_{obo}$
Optocoupler	$I_F = 0$ 80% Rated $V_{CB0}$ $T_A = 125\text{ }^{\circ}\text{C}$	$I_F = \text{rated max}$ Specify $V_{CE}$ to meet max $P_T$ $T_A = 25\text{ }^{\circ}\text{C}$	$\Delta h_{FE}$ $\Delta I_{C(OFF)}$ $\Delta I_{C(ON)}$	$V_{CE(SAT)}$ , $V_{(BR)CE0}$ , $V_F$ $I_{C(OFF)}$ , $I_{C(ON)}$ , $I_R$ , $h_{FE}$ , $t_r$ , $t_f$ , $C_{obo}$

## Leveraging EEE-INST-002

<p>6. Life Testing Subgroup 1</p>	<p>High Temperature Operational Life Testing (HTOL), 125 °C or maximum operating temperature <u>5/</u></p>	<p>MIL-STD-750, Methods as listed</p> <p>For Diode and Bipolar Transistor – TM1027 Steady State Life test for 1000 hours minimum at maximum junction temperature</p> <p>In addition for Power devices: Power MOSFET – TM1042 Cond B Steady State Gate Stress 48 hours</p> <p>Power MOSFET – TM1042 Cond A Steady State Reverse Bias, 240 hours</p> <p>Power MOSFET or Power IGBT Intermittent Operational Life (IOL) TM1042 Cond D or TM1037 for case mount style (e.g., stud, flange, and disc) devices.</p> <p>IOL Number of cycles: From TA = 25 °C, Parts powered to insure <math>\Delta T_J \geq 100</math> °C (not to exceed absolute maximum ratings) = 15000 cycles</p>	<p>16(0)</p>
	<p>Electrical measurement (per device specification) Read and Record</p>	<p>Measure at 25 °C, minimum and maximum rated temperatures</p>	