To: NEPAG
From: Jay Brusse/QSS Group, Inc.
Subject: Survey of MIL Ceramic Chip Capacitor Manufacturers Regarding Termination Finishes

I conducted an informal survey to clarify the termination finishes commonly supplied by the various manufacturers of multilayer ceramic chip capacitors. The MIL specs for these capacitors (MIL-PRF-55681 and MIL-PRF-123) specify several different termination finish options intended for different user applications. In fact, these two specifications are among the few MIL specs I know that still allow for an optional PURE TIN plated termination. Some of the termination finishes are intended for solder mounting others are for conductive epoxy or wire bonding applications. This survey focused solely on the solderable type terminations because the existing termination finish definitions are very loosely defined and can be the source of great confusion amongst the user community. It must be noted that this survey was limited to the primary suppliers of MIL ceramic chip capacitors. In the interest of time, the commercial supplier base for ceramic chip capacitors was NOT surveyed.

As can be seen from the survey results, the "non-specific" MIL specification requirements can lead to a large variation in the termination finishes provided by various suppliers for a given designation. In fact, one manufacturer (AVX) even supplies dramatically different termination finishes for Type W depending on which capacitor style you order.

The survey was actually initiated because of photos recently provided to NASA Goddard by a European user who observed tin whiskers growing on ceramic chip capacitor terminations plated with pure tin. Photos are provided on the following pages for reference. Because of the general acceptance that a tin/lead finish is highly resistant to tin whiskers, it becomes important for users to fully understand what they are ordering/receiving from their suppliers. To help clarify this confusion, a telephone survey was performed of the following 5 QPL manufacturers for MIL-PRF-55681 style chip capacitors.

<table>
<thead>
<tr>
<th>Supplier Location</th>
<th>Contact Person</th>
</tr>
</thead>
<tbody>
<tr>
<td>AVX (Myrtle Beach, SC)</td>
<td>Bob Schmitt / Quality Manager</td>
</tr>
<tr>
<td>Kemet (Simpsonville, SC)</td>
<td>Sandy Campbell / Assoc. Product Assurance Manager Ceramics Dick Thompson / Product Assurance Manager Ceramics</td>
</tr>
<tr>
<td>Vitramon (Bridgeport, CT)</td>
<td>Stu Gillespie / Quality Manager</td>
</tr>
<tr>
<td>Presidio (San Diego, CA)</td>
<td>Christine Pollock / Quality Manager</td>
</tr>
<tr>
<td>ATC (Huntington Station, NY)</td>
<td>Harrison Tarver / Quality Manager</td>
</tr>
</tbody>
</table>

(Compiled by Jay Brusse/QSS Group, Inc. May 2001)
Comparison of Solderable Termination Finishes for Hi-Rel Multilayer Ceramic Chip Capacitors

<table>
<thead>
<tr>
<th>Termination Finish (Designation Per M55681)</th>
<th>AVX</th>
<th>Kemet</th>
<th>Vitramon</th>
<th>Presidio</th>
<th>ATC</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z  Base metallization barrier metal tinned (tin/lead alloy with min. 4% lead)</td>
<td>60/40 Sn/Pb</td>
<td>N/A</td>
<td>N/A</td>
<td>90/10 Sn/Pb</td>
<td>N/A</td>
</tr>
<tr>
<td>Y  Base metallization barrier metal-tin (100% tin)</td>
<td>100% Sn</td>
<td>100% Sn</td>
<td>100% Sn</td>
<td>N/A</td>
<td>N/A</td>
</tr>
<tr>
<td>W  Base metallization barrier metal tinned (tin OR tin/lead alloy)</td>
<td>60/40 Sn/Pb for CDR11 - CDR14</td>
<td>100% Sn for CDR01 - CDR06 CDR31 - CDR35</td>
<td>100% Sn</td>
<td>90/10 Sn/Pb</td>
<td>Sn62</td>
</tr>
<tr>
<td>S  Solder coated final</td>
<td>60/40 Sn/Pb</td>
<td>60/40 Sn/Pb</td>
<td>N/A</td>
<td>60/40 Sn/Pb</td>
<td>Sn62</td>
</tr>
<tr>
<td>U  Base metallization barrier metal solder coated</td>
<td>60/40 Sn/Pb</td>
<td>60/40 Sn/Pb</td>
<td>N/A</td>
<td>60/40 Sn/Pb</td>
<td>Sn62</td>
</tr>
<tr>
<td>Commercial (General Offerings/Some Customer Specific Finishes May Be Available but are not included Herein)</td>
<td>100% Sn</td>
<td>100% Sn</td>
<td>100% Sn</td>
<td>Typically 90/10</td>
<td>Typically Sn62 or 90/10 Sn/Pb Sn62</td>
</tr>
</tbody>
</table>

Notes:
1. The above table was compiled through telephone survey of the listed manufacturers from May 18 - May 22, 2001.
2. The purpose of this table is to highlight the differences in "solderable" (Sn and or Sn/Pb style) termination finishes offered by the competing Military (Hi-Rel) ceramic chip capacitor manufacturers.
3. Other termination finishes such as Pd/Ag, Au, Ag may be offered by these suppliers but are not listed herein because these finishes are not considered to be relevant to concerns with tin whisker formation.

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Photos of Ceramic Chip Capacitors w/ Pure Tin Plated Terminations showing Formation of Tin Whiskers

Extensive details of this application were not available at the time this memo was drafted. What is known is as follows:

- Capacitors use pure tin plated terminations.
- Parts are installed in hybrids using a conductive epoxy.
- User is aware that epoxy mounting of solderable contacts is not a preferred practice, but opted to use this technique based on data provided to them that suggested it could be done reliably.
- User actually ordered parts with Pd/Ag terminations but mistakenly received parts with pure tin terminations. Root cause of this logistical mistake not yet known.
- User is non-NASA affiliated.
- No data yet available regarding part number, date code, manufacturer name, conditions/time that grew these whiskers, observed failures, etc. However, there is extensive research available that generally speaking shows tin whiskers can grow to be several millimeters long and can induce short circuits. NASA Advisory (NA-044 and NA-044A) provide basic info about tin whiskers.

Photos provided by European user circa May 2001

(Compiled by Jay Brusse/QSS Group, Inc. May 2001)