Power Device Qualification Methods

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Introduction

• Radiation effects in semiconductors
  – Power devices are no exception

• Space community is dominated by rad-hard vertical power MOSFETs
  – Little concern about TID effects up to now
  – SEE effects are the current focus

• Facts about SEE in power MOSFETs
  – Shorten lifetime and reduce Safe Operating Area (SOA), i.e., derating or added design margin
  – Ability to block voltage and limit current is most susceptible to SEE effects
  – New designs, application, and requirements are a challenge
  – Very few actual failure in space due to conservative design and low rate
SEE in High-Voltage Power MOSFETs

- **Caused by ion strike**
  - More important for higher-voltage devices
  - Even hardened power MOSFETs are susceptible to SEGR

- **SEGR from two effects:**
  - Direct interaction with gate
  - Increase in electric field from drain
  - Large fraction of voltage in the epi-region is coupled to the gate by the ion strike
  - Dependent on ion angle
  - Failure is high gate leakage

- **SEB from activation of parasitic bipolar transistor under source contact**
  - Temperature dependent
  - Current limitable
  - Failure is high drain-to-source leakage
SEE in High-Voltage Power MOSFETs

- Testing always occurs in situ
- Essentially testing is force a voltage and read a current
- Prompt spike in current signifies a SEE effect
  - Careful distinction must be made from TID effects
  - Small events may not effect parameters

![Graph showing SEE in IRHM3250](SEGR in IRHM3250.png)

**SEGR in IRHM3250**
Kr @ 32 MeV.cm²/mg
Range is 87 µm
Flux = 1.1e4 cm⁻² s⁻¹
SN r0179
Radiation Effects in Power MOSFETs - FY10 (Continuing)

Description:
• NASA missions have driven interest in a realistic derating for power MOSFETs. These missions expose devices to long missions in harsh radiation environments like Geosynchronous, Solar and Jovian missions. The requirements for power management have grown in current and voltage limits while the technology has not advanced, so the need to precisely derate the available devices has increased. Recent work has shown that SEGR is susceptible to dose history, and long and/or harsh missions will reduce the safe operating area of power devices.
• Device types from previous studies that show important phenomena, like atypical proton-gamma correlation, low dose instability in SEE response and hyper-dependence on device architecture, will be chosen for extensive studies to resolve and understand the observations. This will result in a definite derating guideline for the gamut of NASA missions.

FY10 Plans:
• Select two parts that demonstrate the strongest dose-SEGR dependence
  • Procure test devices (IR)
• Test devices with various stressors, including but not limited to
  • proton and gamma irradiation
  • biased and unbiased total dose irradiations
  • SEE testing, SEM and spreading resistance
• Correlated difference with irradiation variable
  • protons vs. gamma
  • biased vs. unbiased
• Provide de-rating guideline

Schedule:

<table>
<thead>
<tr>
<th>Power MOSFET - Radiation</th>
<th>2009</th>
<th>2010</th>
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<td>propose follow-on tests</td>
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Deliverables:
• Report on the SEE failure mode investigation (FY10)
  • Updated SEGR test guideline, if needed (FY10)
• Provide a revised voltage de-rating scheme for power MOSFETs with prior dose history (FY10)

Lead Center/PI: JPL/Selva
Co-Is: JPL/Scheick
Contributors: IR
Center Funding Split: 100%
Radiation Effects in Newly Available Power MOSFETs - FY10 (NEW)

Description:
- Available power devices to NASA missions have decreased due to fabrication challenges at the manufacturer, but new devices are coming onto the available market. Very little radiation test data applicable to NASA mission is available on these devices. Upcoming NASA missions have driven interest in a better catalogue of parts available to NASA designers and contractors with adequate mission assurance data.
- Device types for an emerging manufacturer (Fuji) will be procured pro bono to test to NASA standards (Testing Guideline for Single Event Gate Rupture (SEGR) of Power MOSFETs). Applicability to NASA mission will be assessed with any derating guidelines.

FY10 Plans:
- Acquire parts
  - Pro-bono test hi-rel test devices from Fuji
  - 500V from Fuji is in works
  - 3 other part types expected
- Perform radiation testing
  - SEGR and TID/DDD
- Analyzed data
  - Side by side comparison with IR
  - FIT and SER estimates
  - Any circuit application anomalies

Schedule/Costs:

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Deliverables:
- Report on the SEGR results for new device types (FY10)

NASA and Non-NASA Organizations/Procurements:
- Test devices: $0K
- Equipment upgrade: $0K

Lead Center/PI: JPL/Selva
Co-Is: JPL/Scheick
Contributors: STM, IR
Center Funding Split: 100%
Radiations Effects in Power Devices
Timeline

- Proactive and reactive response to technology challenges
  - Proactive in qualifying device ahead of missions
  - Reactive in concerns of upcoming missions

- 2004 – Technology overview
- 2005 – Test method development
- 2006 – Latent damage investigation
- 2007 – Test method publication
- 2008 – Dose damage investigation
- 2009 – Qualify emerging part vendors
- 2010 – Test guide update and IR requal
- 2011 – Emerging vendors and technology
Goals

• Test emerging devices that are usable to community
  – IR, Microsemi, Semicoa, STM, Fuji
  – GaN MOSFETs
• Investigate observed radiation anomalies in power MOSFETs
  – Latent damage, dose effects, lifetime effects
• Develop testing methods and technology to meet new emerging project needs
  – Extremes in current and voltage measurement
  – Specific environment testing
    • Thermal or radiation emulation
  – Enhanced predictive methods including
    • Predicted rate calculation and device modeling
• Inform public on what methods are best
  – NASA test guideline
  – Report latest testing (NSREC Data Workshops)
Expected Impact to Community

• Mission Assurance
  – Improved test capabilities
    • Both in NASA capabilities and exported in guidelines
  – Improved mitigations and analysis
    • Rate prediction is a continuing challenge

• Design applications
  – Improved derating and part applications
    • Less time spent on part selection
  – Part selection based on known data

• Product development
  – Better data
    • Prompt feed back to vendors
Status/schedule

- **Updating latest test guideline**
  - The Test Guideline for Single Event Gate Rupture (SEGR) of Power MOSFETs” [JPL Publication 08-10 2/08].
  - Q3-FY10

- **Publish ASTM guide for power device testing**
  - Q4-FY11

- **Qualify emerging parts**
  - New IR line and other entries
  - Q4-FY11

- **Technology readiness for emerging technology**
  - GaN and SiC
Highlights/Accomplishments

• Part qualification
  – Supported or partially supported testing of more than 30 power MOSFETs in 6 years

• Anomaly investigations where risk to flight project was scoped
  – Latent damage in power devices
    • Manageable risk
  – Effect of dose on SEE response
    • Minimal risk
  – Criticality of testing conditions on failure rate estimation
    • Significant risk
  – Effect of circuit on SEE response
    • Manageable risk
Highlight 1 – Gross SEE Data

- Data delivered to public
  - SOA (left) to aid in part selection
  - Failure mode data (right) to aid in mitigation

- Risk on each part is manageable
Highlight 2 – Latent damage

- 200V Power MOSFET used at limit of SOA
- Range of test ion an issue
- Additional testing revealed non-catastrophic breaks within SOA
- Question of the resulting risk of “walking wounded” devices was issue
- Further analysis would estimate that the risk of enough incremental damage was low
- Reduction in life not known

![SEGR in a 200-V Hardened Power MOSFET](chart.png)
Highlight 2 – Latent damage – Fatal stress

Walking Wounded

The combination of bias, current, and thermal stress induce failure. Applications are advised to avoid even small order events. Risk is manageable.
Highlight 3 – Environment Stress effect on SEGR

- Missions currently in design
  - Greater than 10 year duration
  - Mission dose over 10 krad(Si)
  - High power and voltage requirements
  - Power devices not used in these instances
- Question has come up (repeatedly) of any synergy from these factors
- Dose and stress change device parameter that affect SEGR

![Graph showing the relationship between proton fluence and transconductance, threshold voltage, and IRHM8450 at V_Ds=10V, I_Ds=1A.](image)
Highlight 3 - Environment Stress effect on SEGR

• The reduction in SOA due to damage from radiation is observable
  – However, decrease for rad hard device is contained in normally used derating

Normally used derating envelops margin from effect.

Risk is minimal.
Highlight 4 – Test conditions effect on failure rate calculations

- Manufacturers data on 1000V MOSFET underestimated SEGR due to lack of ion range in tested devices
- Retesting resulted in significantly reduced safe operation area (SOA)
- Redesign of application was costly
- Ion energy key in testing
- Question of what derating or weighting function must be applied to low energy ion to offset energy

Weighted average LET (MeV cm²/mg) for swift heavy ions traveling through a virtual power MOSFET composed of 3 μm of metallization over 75 Å of SiO₂ and a Si epitaxial layer of various depths, i.e., 15, 26 and 40 μm. The ions used in this study are similar to the short range ions from Brookhaven National Laboratory and the long range ions from Texas A&M.
Highlight 4 – Test conditions effect on failure rate calculations

![Graph showing voltage for gate rupture vs. LET (MeV·cm²/mg)](image)

- Data with long-range ions
- Manufacturer's data with short-range ions

- Manufacturer's data

Highlight 4 – Test conditions effect on failure rate calculations

• The experimental conditions have a considerable effect on the expected failure rate

• Table shows a rate from Xenon failures for 200V part operated at 150V and zero gate bias for four testing conditions

• Data show a minimum in $V_{crit}$ with ion energy

<table>
<thead>
<tr>
<th>Ion Energy [MeV/amu]</th>
<th>Ion LET [MeV.cm$^2$/mg]</th>
<th>Ion range in Si [um]</th>
<th>Rate* [per day]</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.64</td>
<td>69</td>
<td>~40</td>
<td>6.5x10^{-9}</td>
</tr>
<tr>
<td>10.6</td>
<td>57.5</td>
<td>~100</td>
<td>3x10^{-7}</td>
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*Rate is per unit cross-section.

Risk in using the wrong test data is significant.
Conclusion

• Power device technologies still suffer from growing pains in regards to radiation effects
  – Higher rated parts may be limited by radiation effects
  – Alternates are scarce
    • SiC, IGBTs, SCRs all have liabilities
  – Derating (Design margin) on the SOA is the most used approach

• Lesson learned
  – New applications yield uncovered effects
    • All radiation issue should be revisited after new design
    • Or new environment, technology, or mission profile