

# The NASA Electronic Parts and Packaging (NEPP) Program – Automotive Electronics

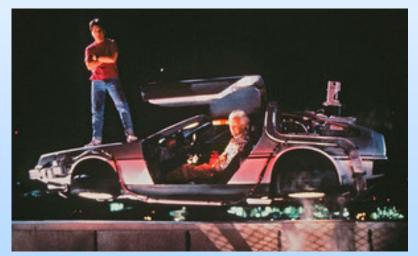
Kenneth A. LaBel ken.label@nasa.gov 301-286-9936 Co- Manager, NEPP Program NASA Goddard Space Flight Center (GSFC) http://nepp.nasa.gov

To be published on nepp.nasa.gov previously presented by Kenneth LaBel at the NASA Electronic Parts and Packaging (NEPP) Electronics Technology Workshop (ETW), Greenbelt, MD, June 17-19, 2014.



# Outline

- Acronym List
- Why is NASA Interested?
- Plans for FY14/FY15
- Summary



http://static.nme.com/images/article/ BackToTheFutureCar600Gb120712.jpg



# Acronyms

Acronym	Definition
AEC	Automotive Electronics Council
AEC	Automotive Electronics Council
BOK	Body of Knowledge
COP	Community of Practice
FY	Fiscal Year
ICs	Integrated Circuits
IP	Intellectual Property
NASA	National Aeronautics and Space Administration
NEPP	NASA Electronic Parts and Packaging
SOC	Systems on a Chip
ті	Texas Instruments



#### NEPP -

### **Automotive Electronics Considerations**

- As a means of potentially providing a more cost-effective choice of electronics for certain classes of missions, NEPP is evaluating the use of automotive grade electronics.
- NEPP has three tasks in plan:
  - Develop a body of knowledge (BOK) document, highlighting the Automotive Electronics Council (AEC) documents as well as discussions with manufacturers (Mike Sampson, Jay Brusse).
  - Evaluate (reliability) selected automotive grade electronics (in collaboration with Navy Crane).
    - ICs (Shri Agarwal),
    - Capacitors (Jay Brusse), and,
    - Discretes (Benny Damron).
  - Evaluate (radiation/reliability) automotive grade microcontroller (Ken LaBel, acting).
- Overall task area leadership
  - Mike Sampson

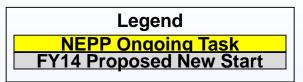
To be published on nepp.nasa.gov previously presented by Kenneth LaBel at the NASA Electronic Parts and Packaging (NEPP) Electronics Technology Workshop (ETW), Greenbelt, MD, June 17-19, 2014.



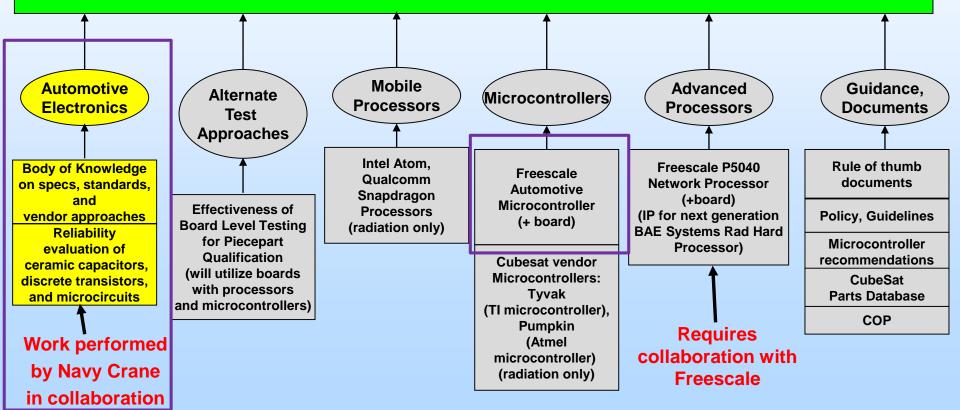
#### FY14 NEPP Core –

#### **Automotive/Commercial Electronics (Small Missions)**

**Core Areas are Bubbles**; Boxes underneath are variable tasks in each core



#### **NEPP Research Category – Automotive/Commercial Electronics**



To be published on nepp.nasa.gov previously presented by Kenneth LaBel at the NASA Electronic Parts and Packaging (NEPP) Electronics Technology Workshop (ETW), Greenbelt, MD, June 17-19, 2014.



# **Automotive Microcontroller**

- Plan is to utilize automotive grade microcontroller (32-bit, 90 nm) fabricated by Freescale on-shore.
  - Utilizes same software development tool as future multicore processors (see Systems on a Chip (SOC) task area).
- Both radiation and reliability evaluations planned.
  - Will utilize evaluation boards for radiation tests.
  - Will utilize both piecepart and evaluation boards for reliability tests.
    - Outcome provides comparison between test techniques.
    - Additional active and passive components on evaluation board will be tested.
- May provide recommendation for usage in instruments/small mission applications.

Evaluation ICs
Image: Constraint of the second second

## Radiation Microcontroller

**ACTIVITY** 

BOK

**Reliability Microcontroller** 

#### Multiple vendors for ICs, Capacitors, and Discretes

To be published on nepp.nasa.gov previously presented by Kenneth LaBel at the NASA Electronic Parts and Packaging (NEPP) Electronics Technology Workshop (ETW), Greenbelt, MD, June 17-19, 2014.



PERIODS

S

Ο

Ν

D





M A

Μ

F.





- NEPP is exploring possible usage of alternate grade electronics as a means of cost/performance effectiveness.
- FY15 plans to begin looking at medical grade.



Family member of microcontroller of interest

http://johndayautomotivelectronics.com/freescale-qorivva-mcustarget-next-generation-body-control-and-gateway-applications/