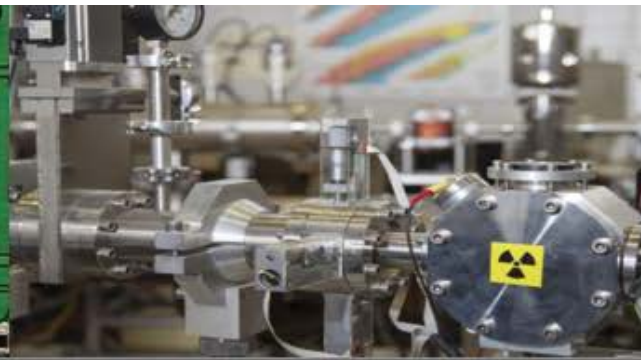


NEPP ETW 2020

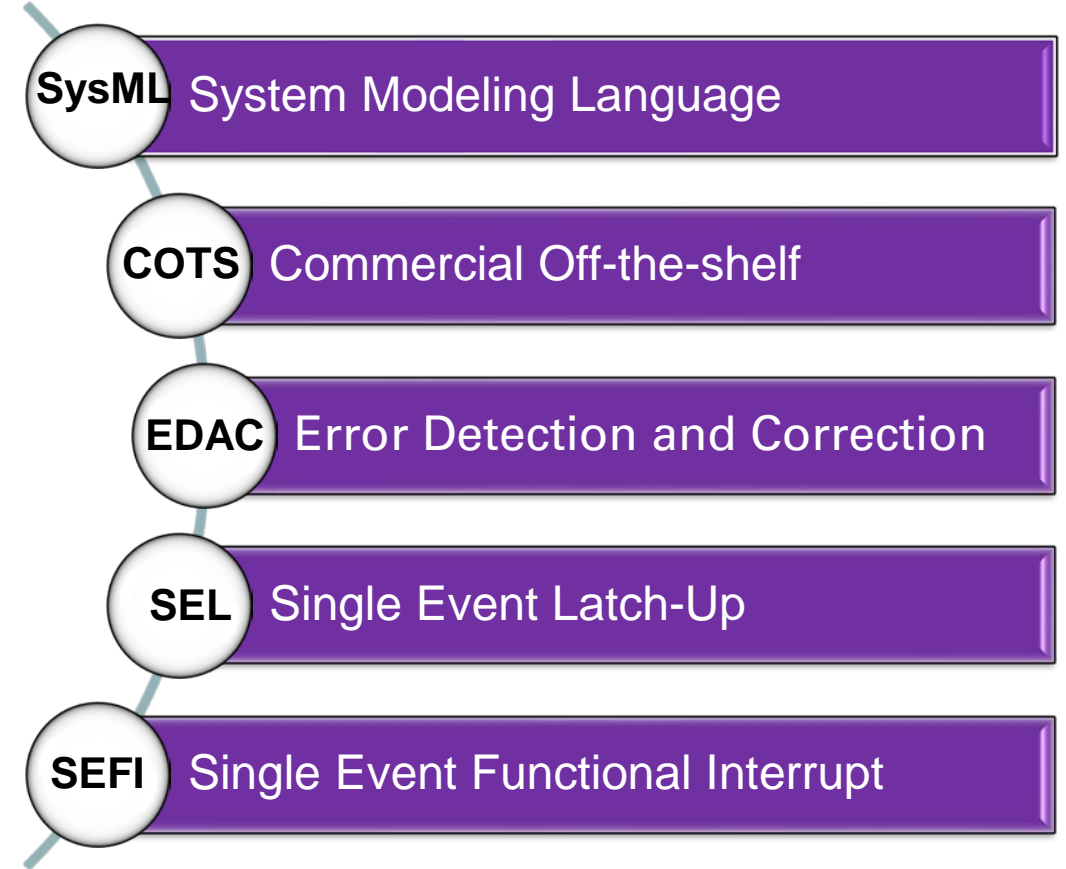
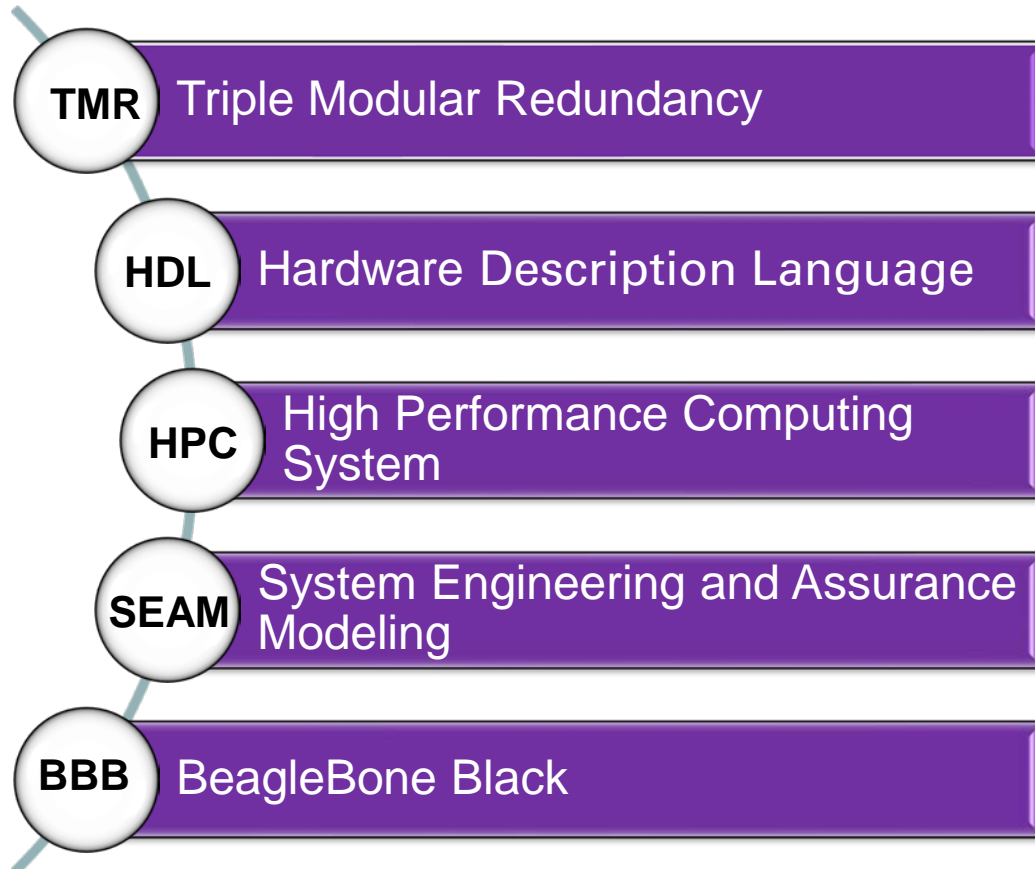


Application of SEAM to High-Performance Computing

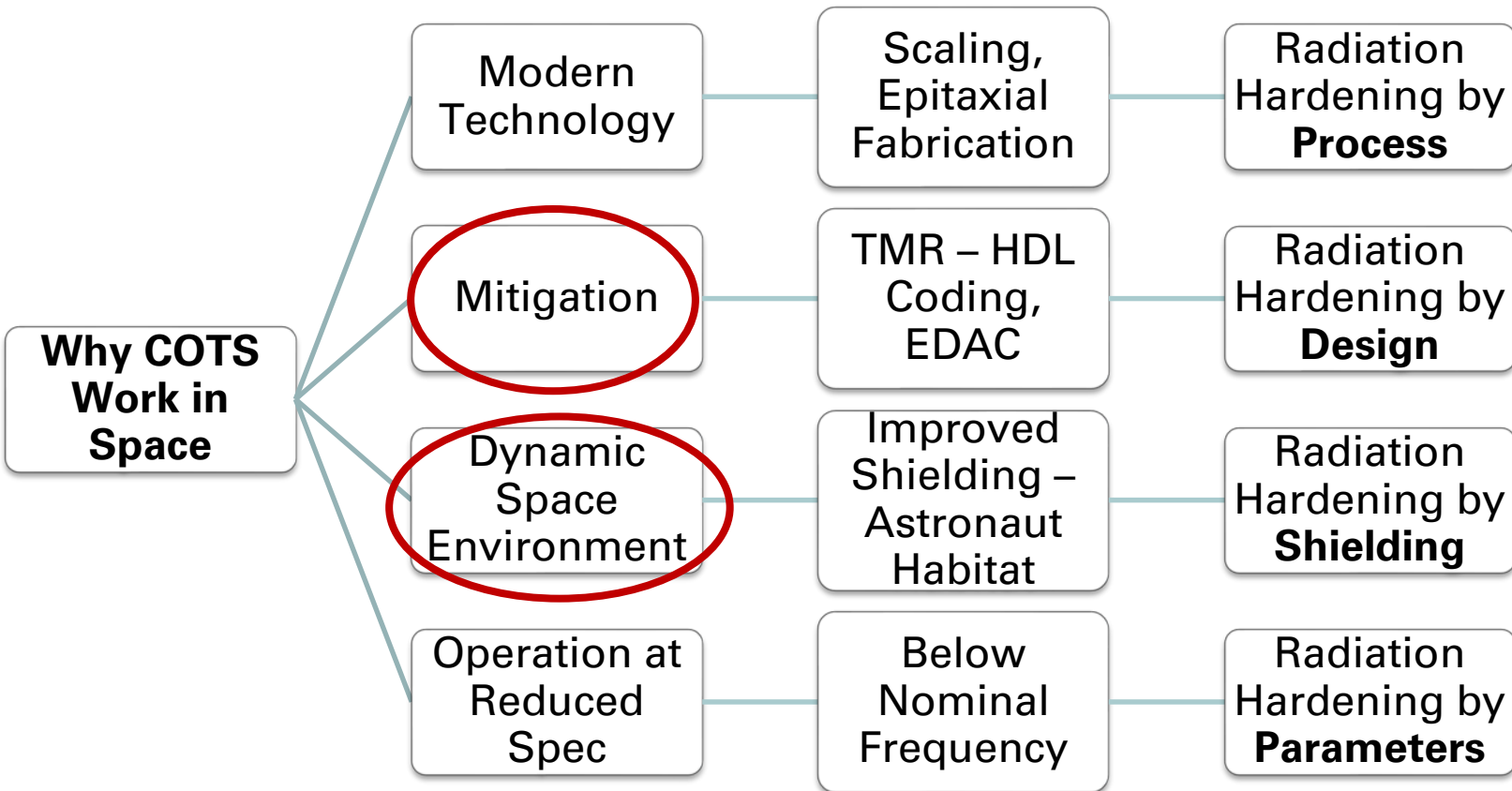
**M. Reaz, A. Witulski, B. Bhuva, N. Mahadevan, G. Karsai,
B. Sierawski, R. Reed, R. Schrimpf
Vanderbilt University**



Acronyms and Abbreviations



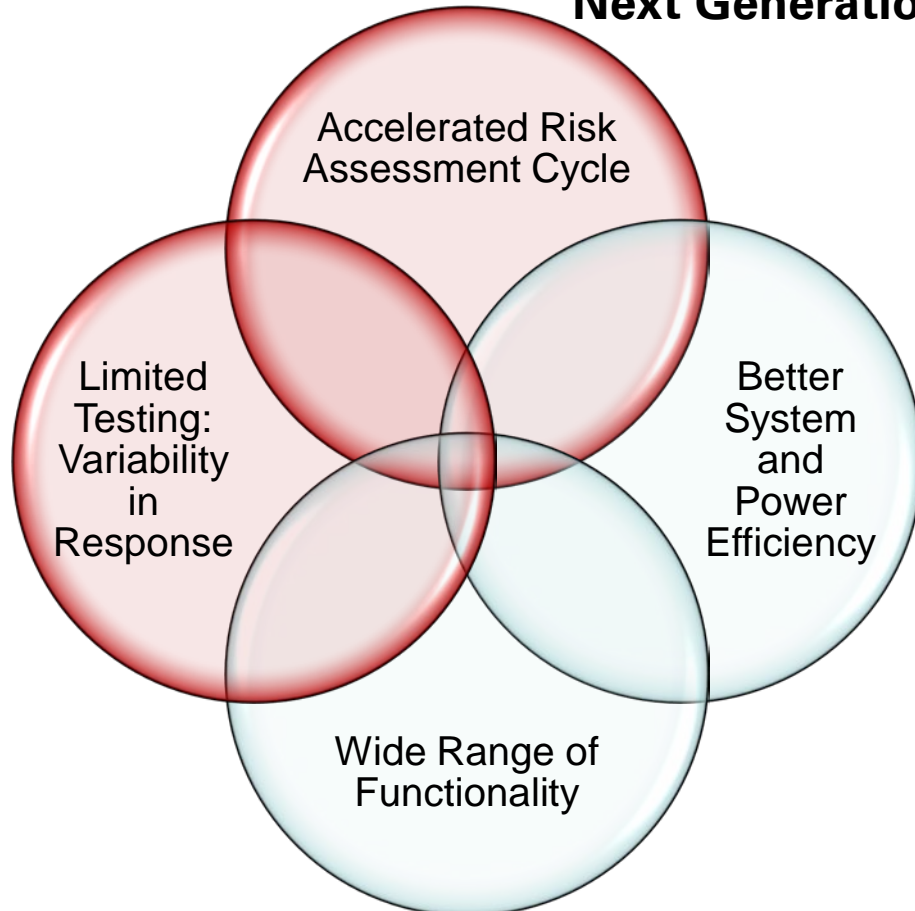
Performance: COTS Components under Radiation in Space



Devices in space that are primarily made of COTS component

Performance: COTS Components under Radiation in Space

Next Generation System in Space with COTS Component



- Utilizes heavy shielding inside astronaut habitat
- In house data/ image processing
- Low communication bandwidth
- Reduced cost
- Faster data transfer

Predicting performance using model-based assurance

- Represent the system with a high-level logical model
- Validate the model to experiments
- Identify the out of specification behavior of the components
- Mitigate the existing fault or reduce the mission requirement or replace the component with a radiation hardened one.

- ❑ What is the minimal change in design/ component that would improve the reliability of a system with COTS component in space
- ❑ Will software-based mitigation suffice?

STTR Phase I : Testing of COTS Systems in Space Radiation Environments



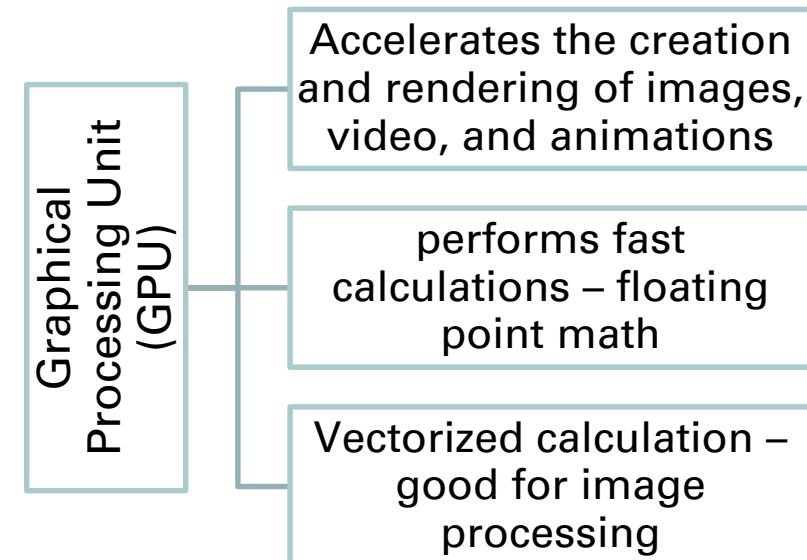
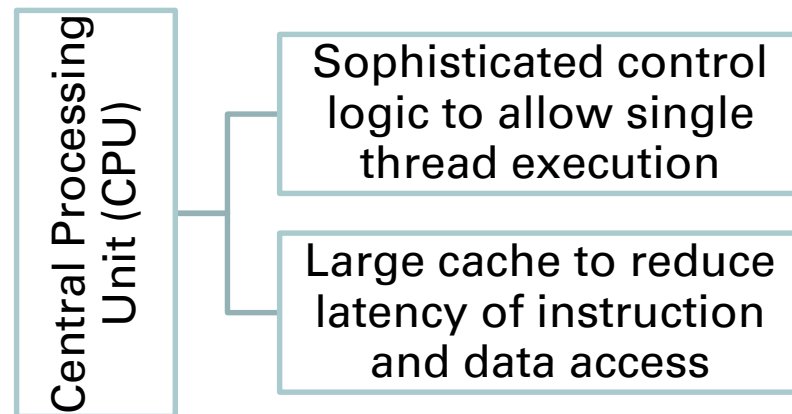
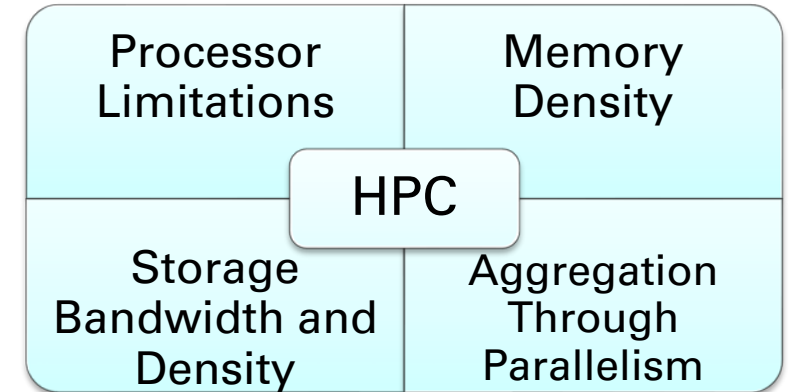
Vanderbilt Engineering

- NASA STTR 2019 Phase I Solicitation from Langley Research Center
- T6.05 Testing of COTS Systems in Space Radiation Environments

RFP: Investigate the feasibility of COTS electronics for *High Performance Computing (HPC) in space environments which are already heavily shielded*. It seeks strategies *based on a complete system analysis of HPC COTS* that include, but not limited only to, *failure modes* to mitigate radiation induced impacts to potential HPC systems in those highly shielded space environments.

High Performance Computing (HPC)

- Computing in parallel over lots of compute elements
- Make many systems look and work like ONE large, powerful system
- Used for highly computational or data-intensive tasks
- Scalable parallelism is the key for running advanced programs efficiently, reliably and quickly
- Accelerates the creation and rendering of images, video, and animations. Performs fast math calculations

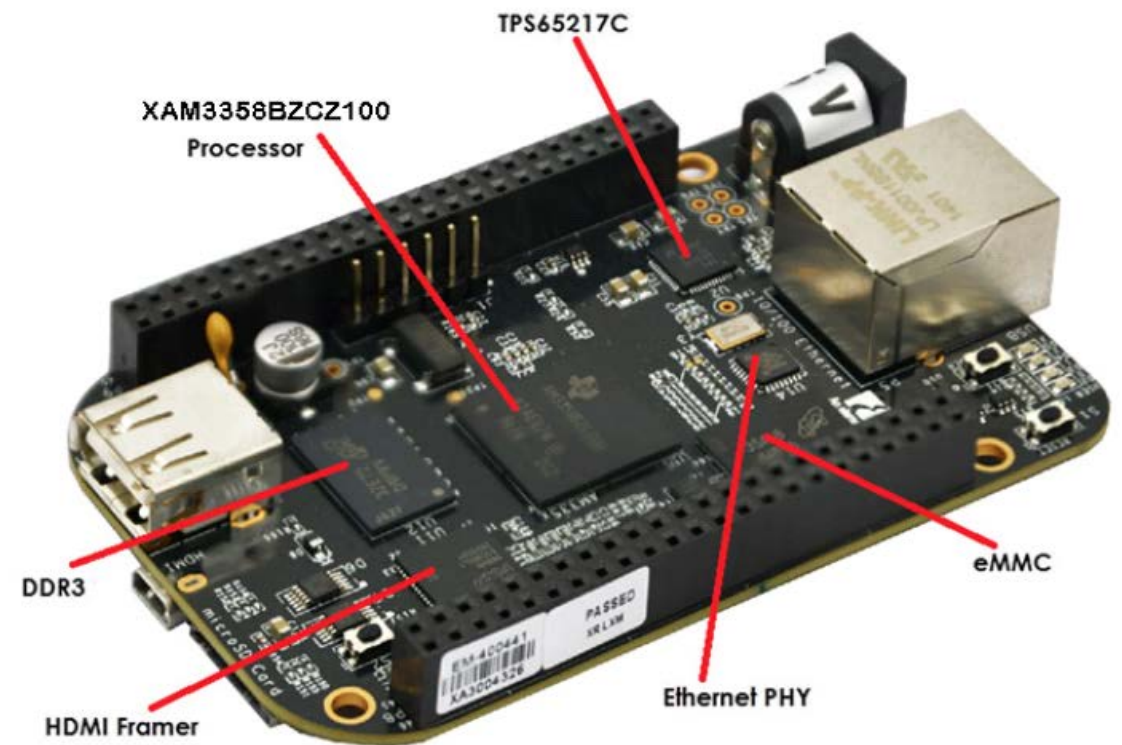


Candidate Demo HPC System: BeagleBone Black (BBB)

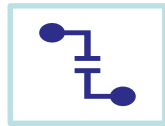
- CPU – Integer Core
- GPU – Neon Core (Floating point calculation, vectorized data processing)
- Memory – Cache, SRAM, EEPROM, FLASH, MMC, SD, ECC
- Interface – μ SD, μ HDMI, Ethernet, JTAG, GPIO, PWM, Serial, SPI, and I2C

- Open source community
- No heatsink on processor-SE Tests
- Good availability
- Low Power – Arm Instruction

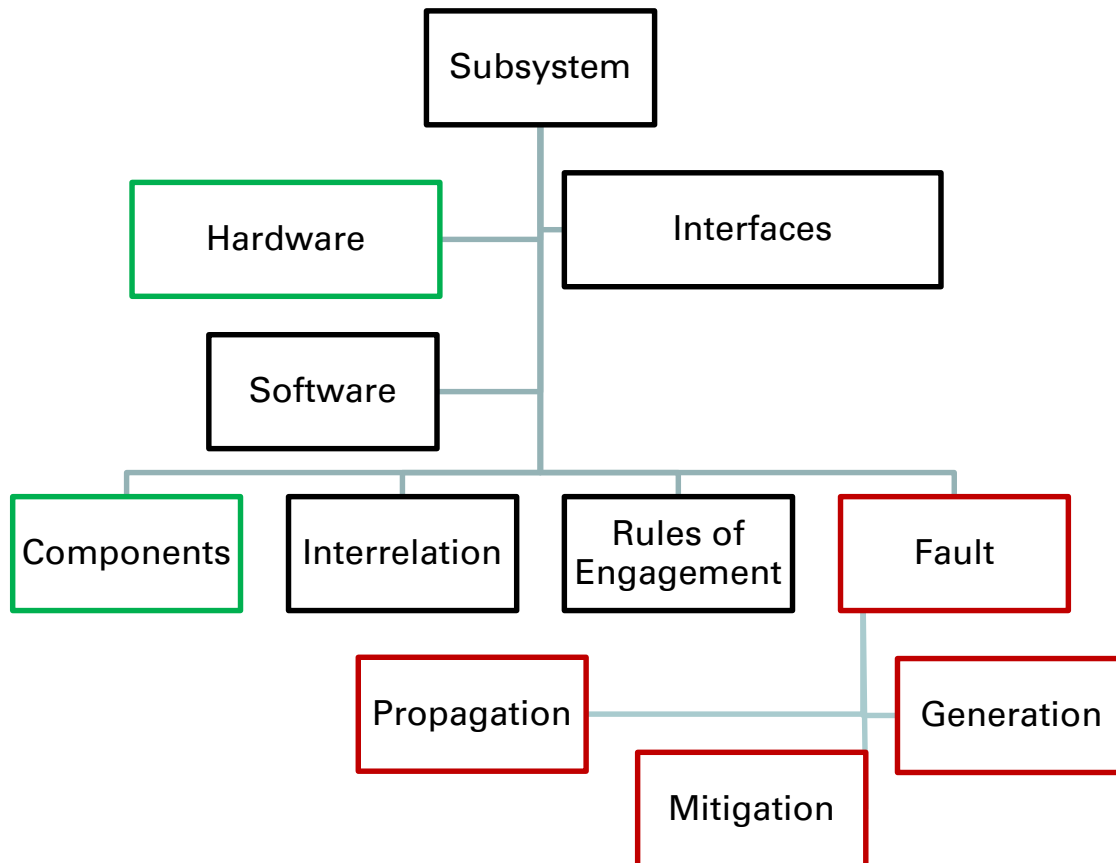
BBB ~ smallest building block of high-performance computing



Model Based Assurance using SEAM for Complex Systems



Architectural Model



Architectural Model : Define the rules of engagement for fault propagation through physical components and ports in a complex system

Goal : Calculate the sensitivity of faults to system response in a heterogeneous architecture

Challenge : Minimum cut set of failure at the component level is not definitively understood in a complex system

Proposed Solution : Validate the models by functional decomposition in conjunction to experiment

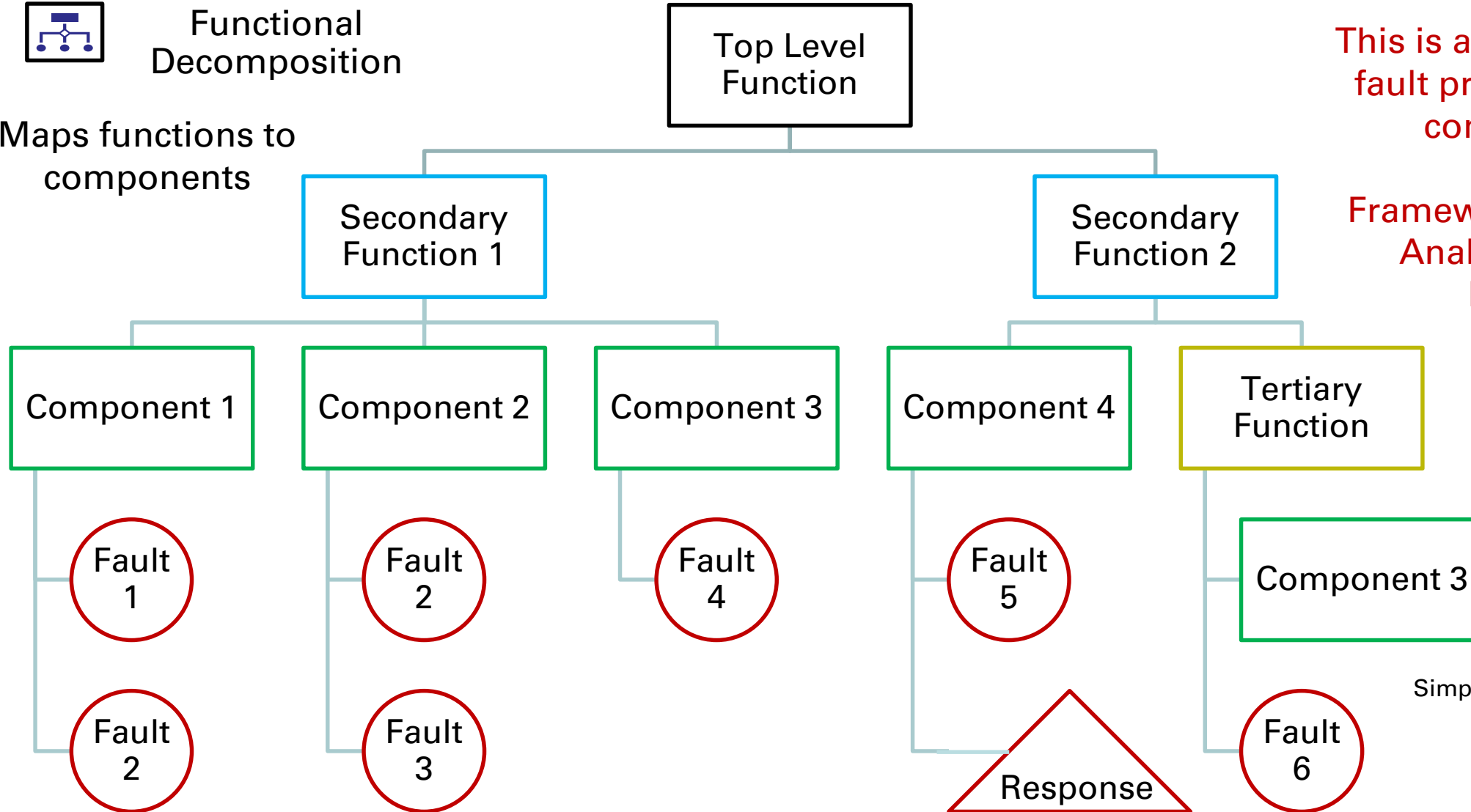
Framework for Failure Analysis

Model Based Assurance using SEAM for Complex Systems



Functional Decomposition

Maps functions to components

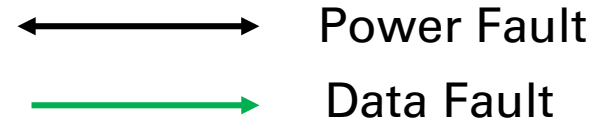
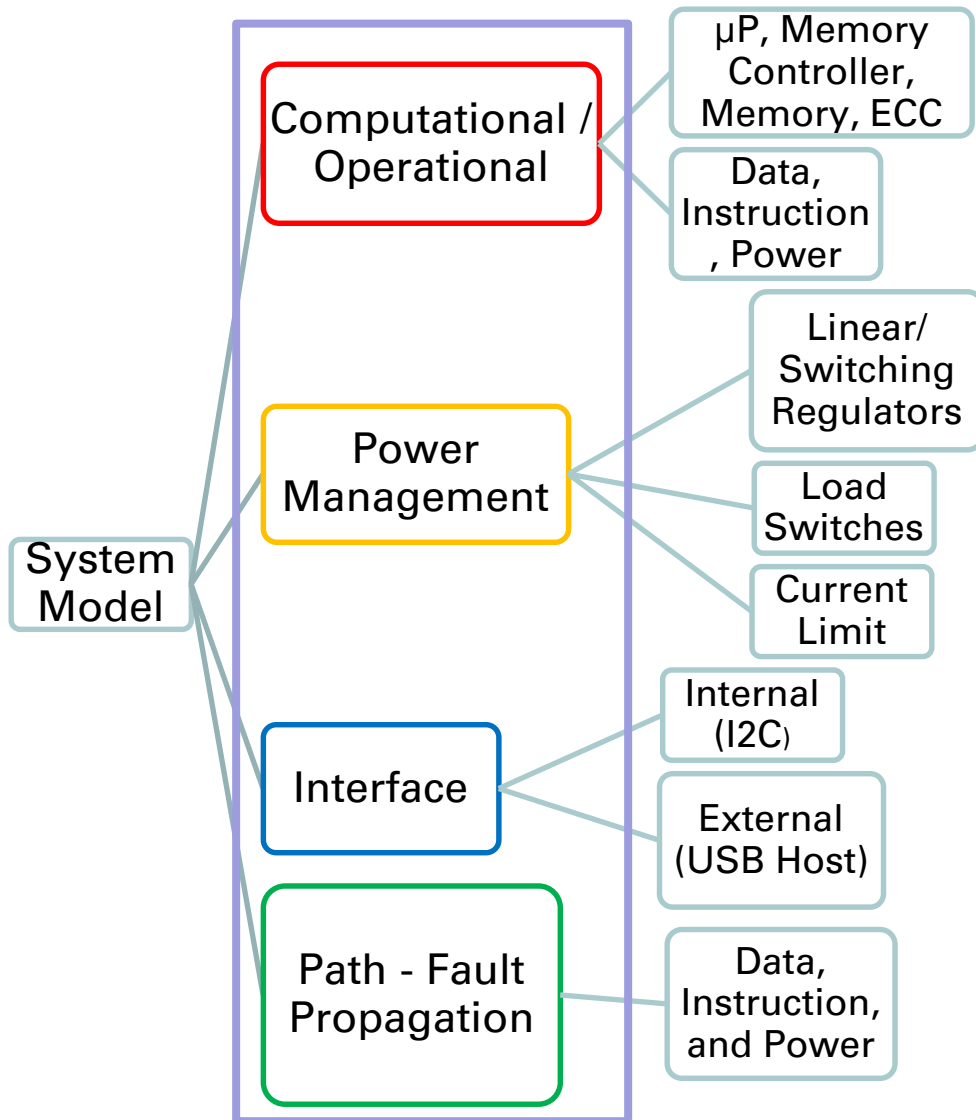


This is a logical model where fault propagates instead of conventional data.

Framework for Probabilistic Analysis : Fault Tree / Bayesian Net

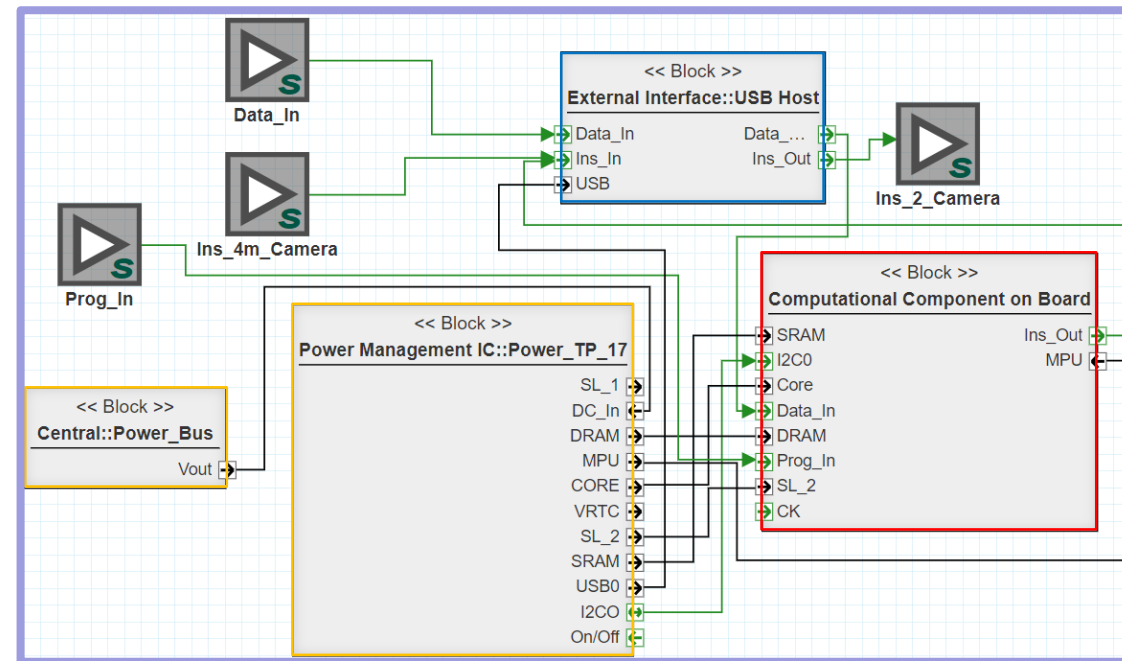
Simplified diagram shown here for brevity

Architecture- HPC System: BeagleBone Black (BBB)

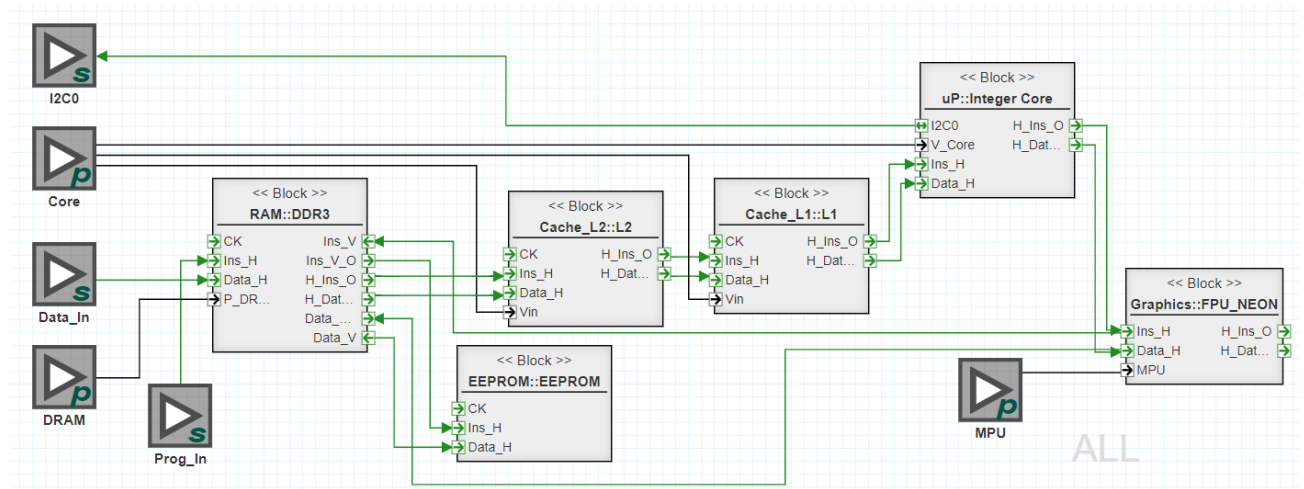
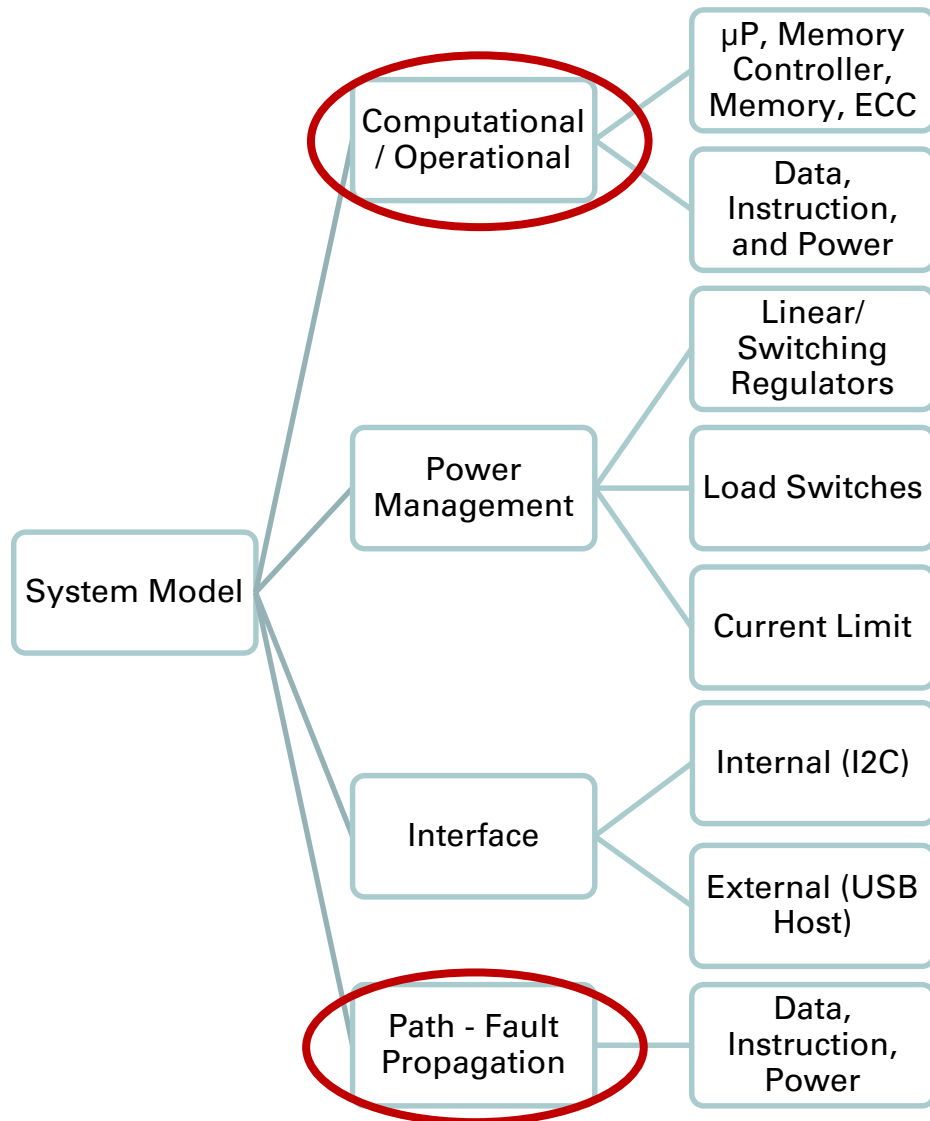


Complex cycles of operation : integer/ floating point core, memory management

How to model complex system with limited information?



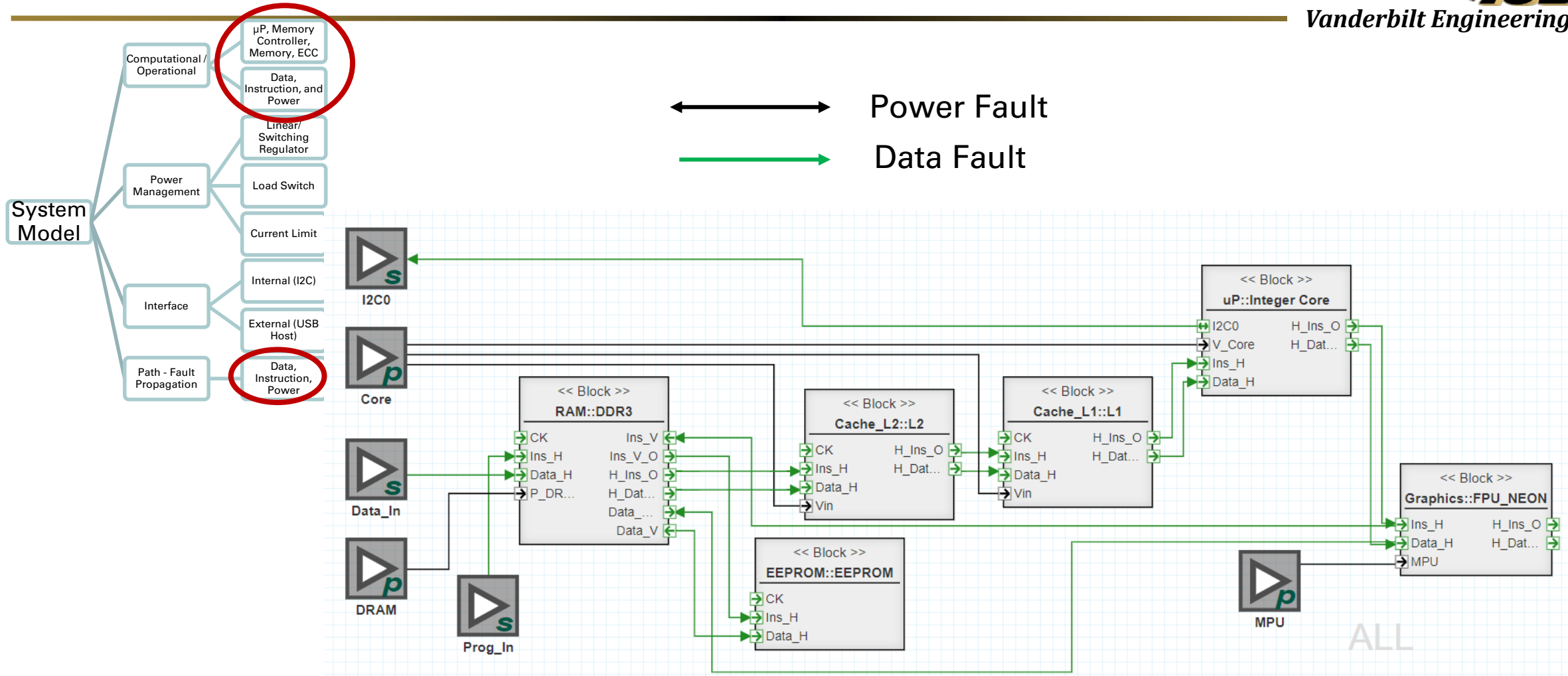
Computational Components in HPC



This is a logical model where fault propagates instead of conventional signal.

Framework for Probabilistic Analysis : Fault Tree / Bayesian Net

Computational Components in HPC



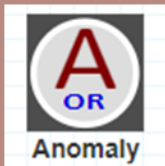
Definitions : Logical Modeling of Failure Propagation



Fault Propagation Models show how fault originate in components and their effects propagate through the structure of the system.



The radiation-effects such as 'TID' and 'SEL' are captured as **fault: 'F'** nodes

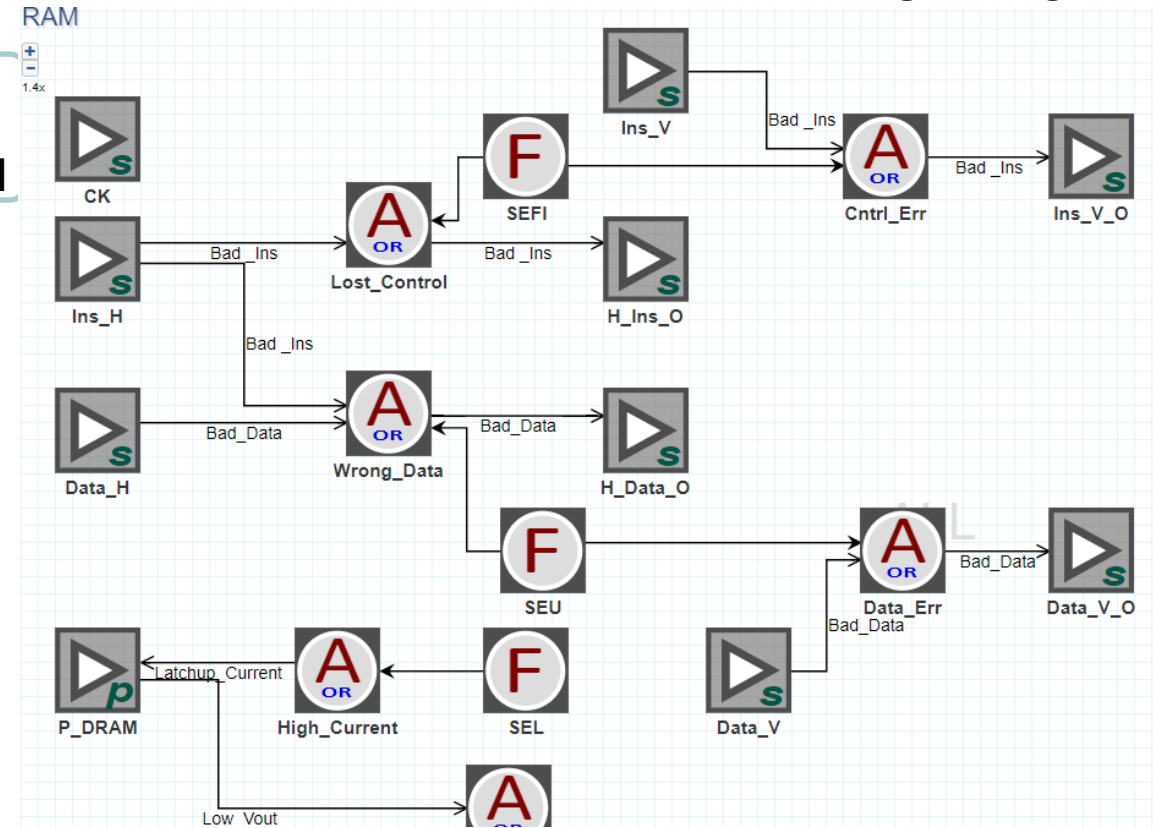
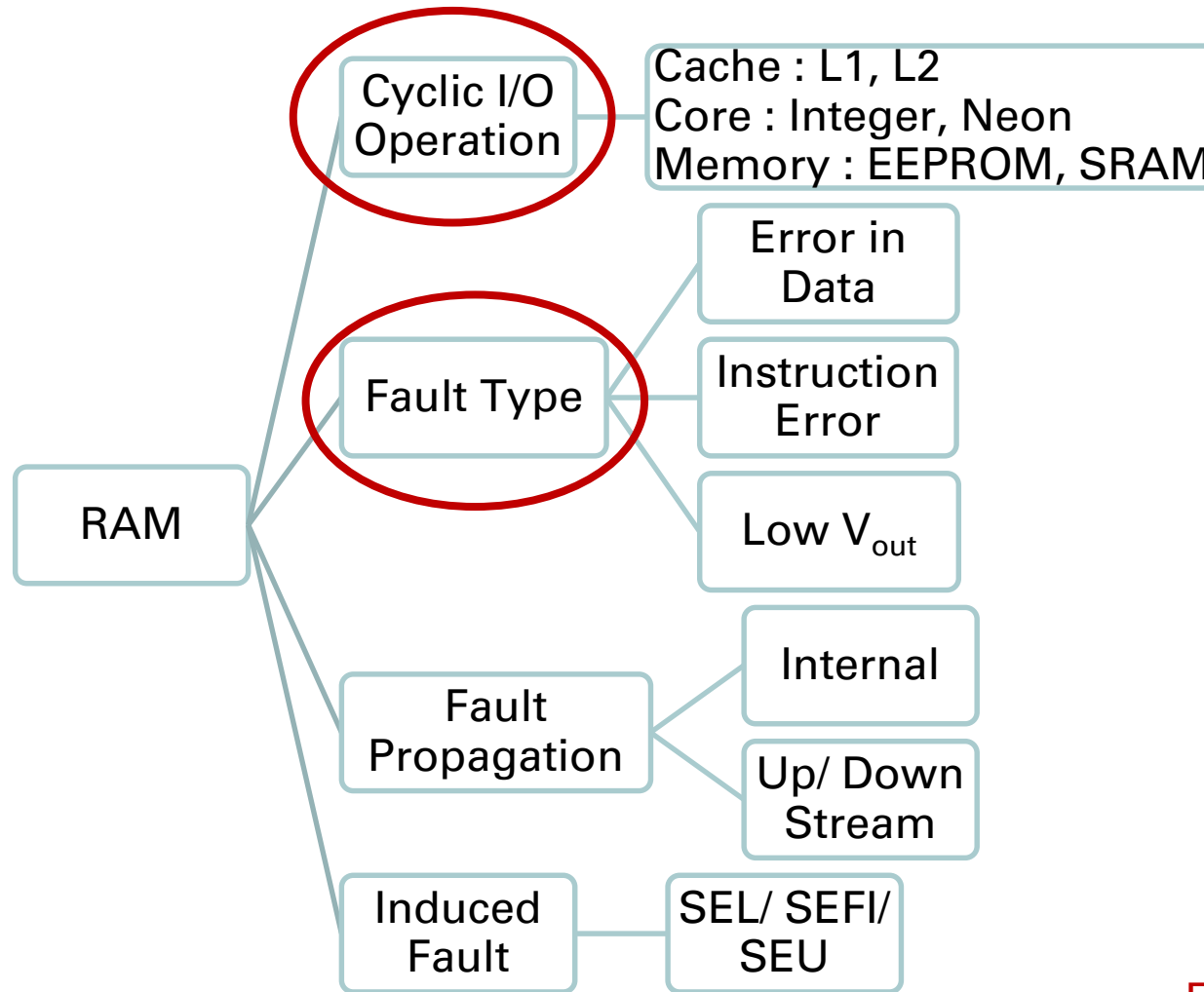


Fault ('F' nodes) leads to **anomalies ('A' nodes)** such as 'Bad_Data' or 'Low_Vout'



Anomalies lead to the functional response effects representing 'Degraded Operations' or 'Mitigation Mechanisms'

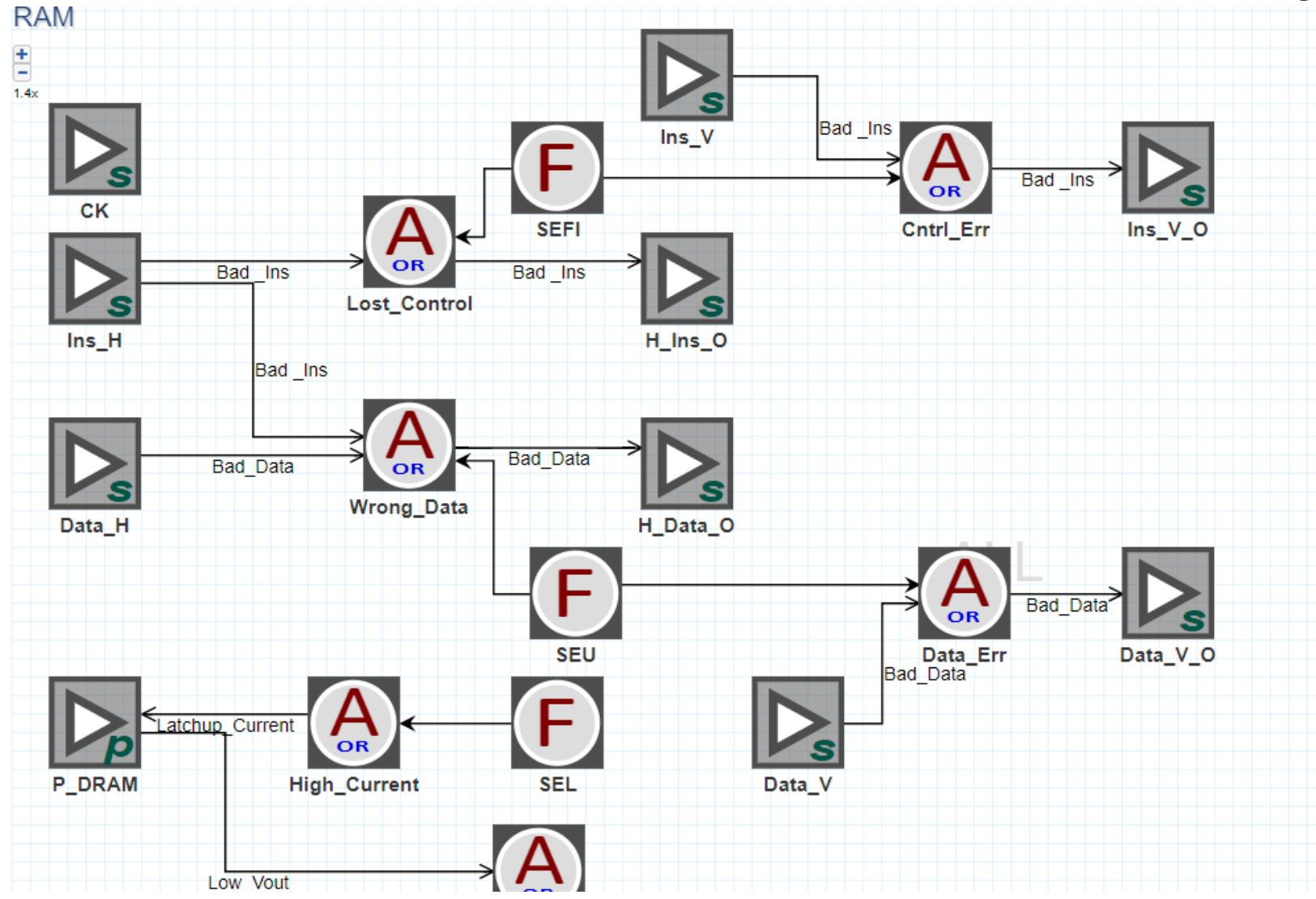
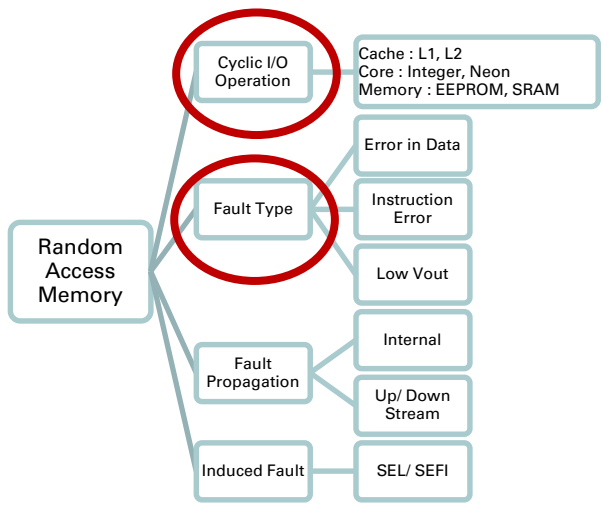
Heterogeneous Dependence of Failure: Random Access Memory (RAM) - BBB



This is a logical model where fault propagates instead of conventional signal.

Framework for Probabilistic Analysis : Fault Tree / Bayesian Net

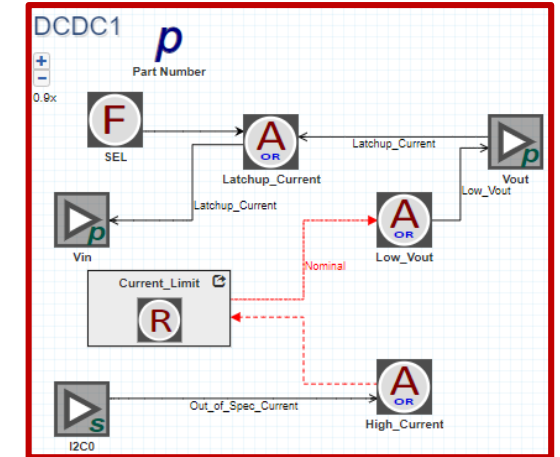
Heterogeneous Dependence of Failure: Random Access Memory - BBB



Critical Component to Failure: Power Management IC - BBB

System is highly sensitive to power faults

- Low V_{out} to components (Downstream)
- High Current to Regulators (Upstream)
- Mitigation (Current Limit/ Power Reset)



I2C Port

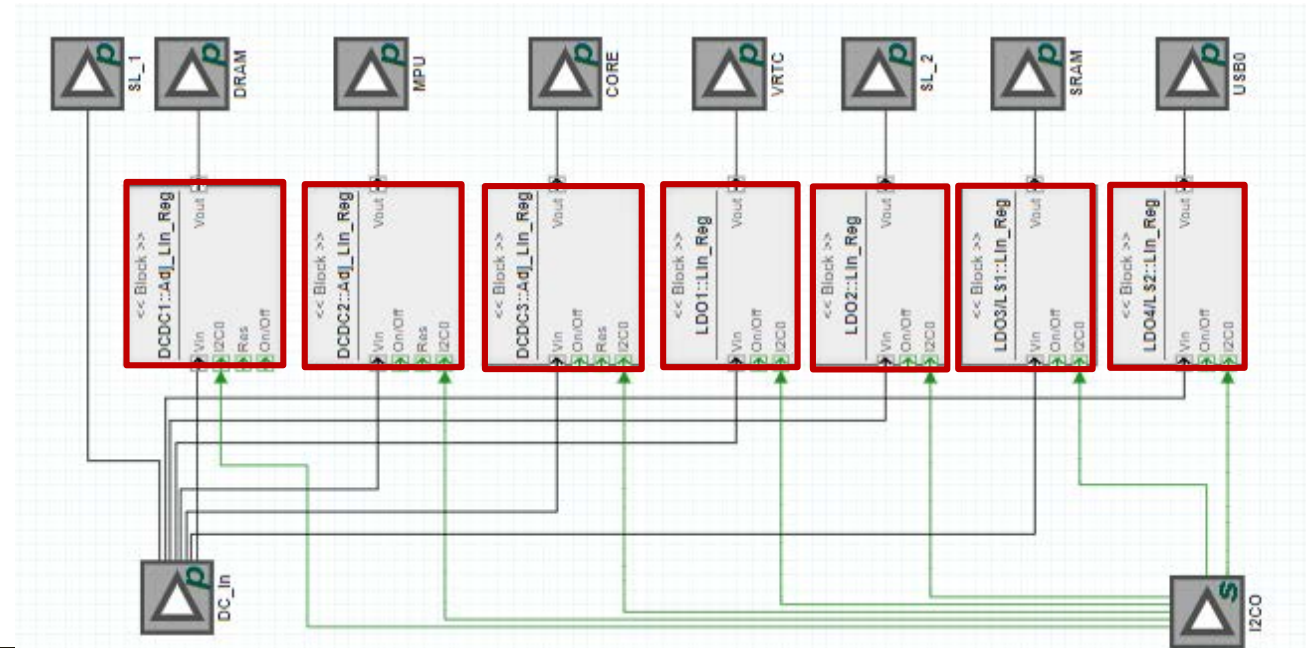
- Communication with Integer Core

Primary Component

- Linear Regulator/ Logic Switch

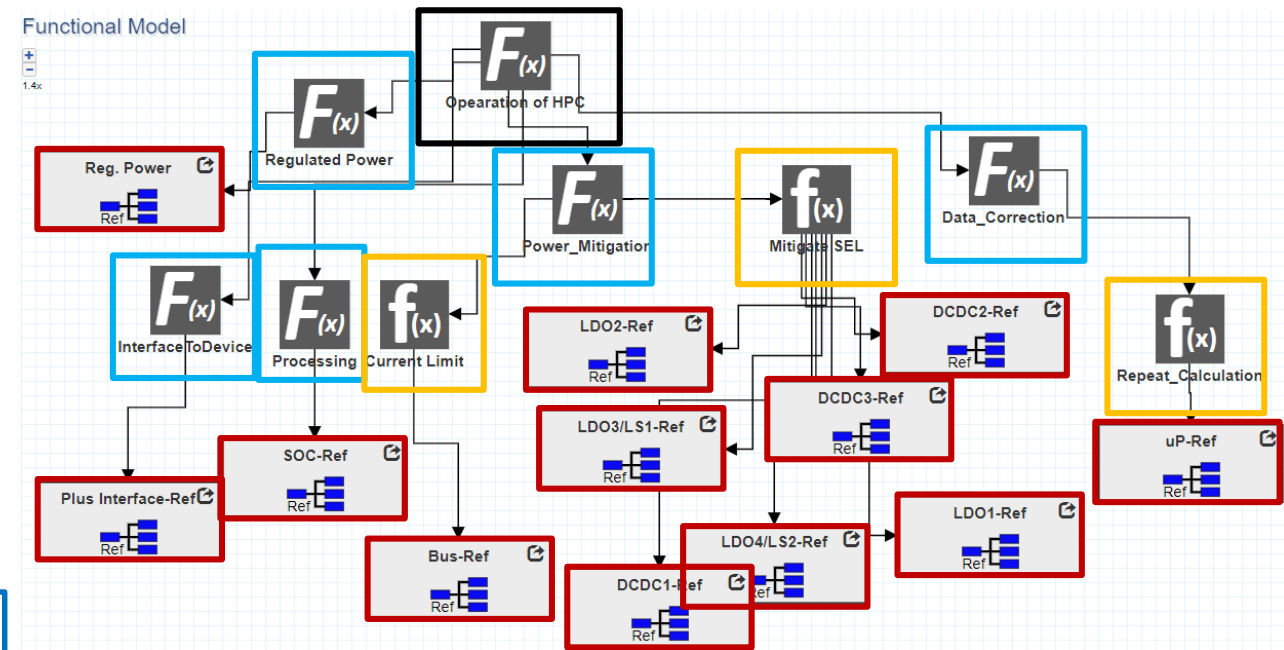
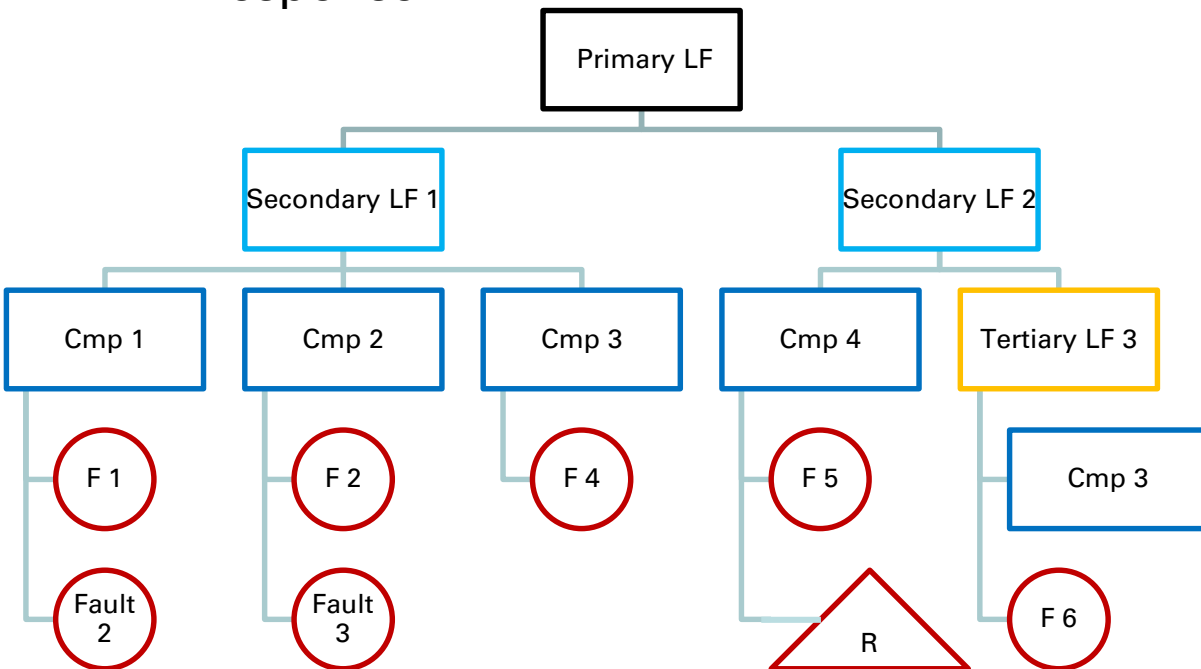
Instructions of Current Control

- Integer Core



Functional Decomposition of the Operation of the BBB

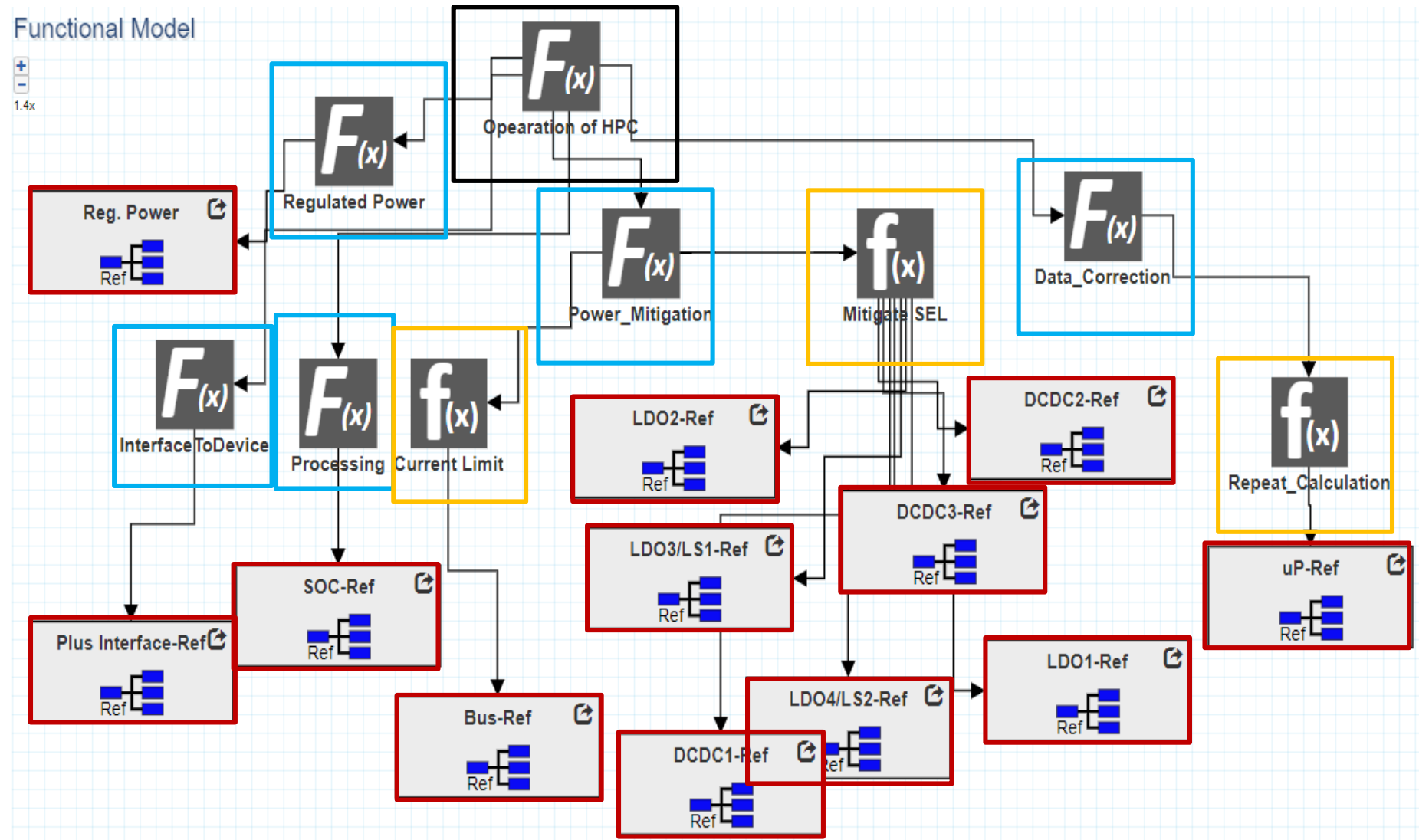
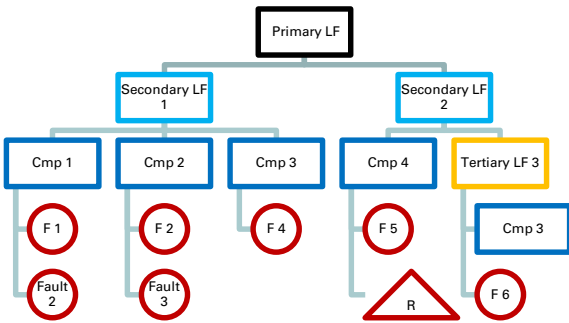
LF : Lost Function
 Cmp : Component
 F : Fault
 R: Response



Maps Functions to Components

Framework for Probabilistic Analysis : Fault Tree / Bayesian Net

Functional Decomposition of the BBB System

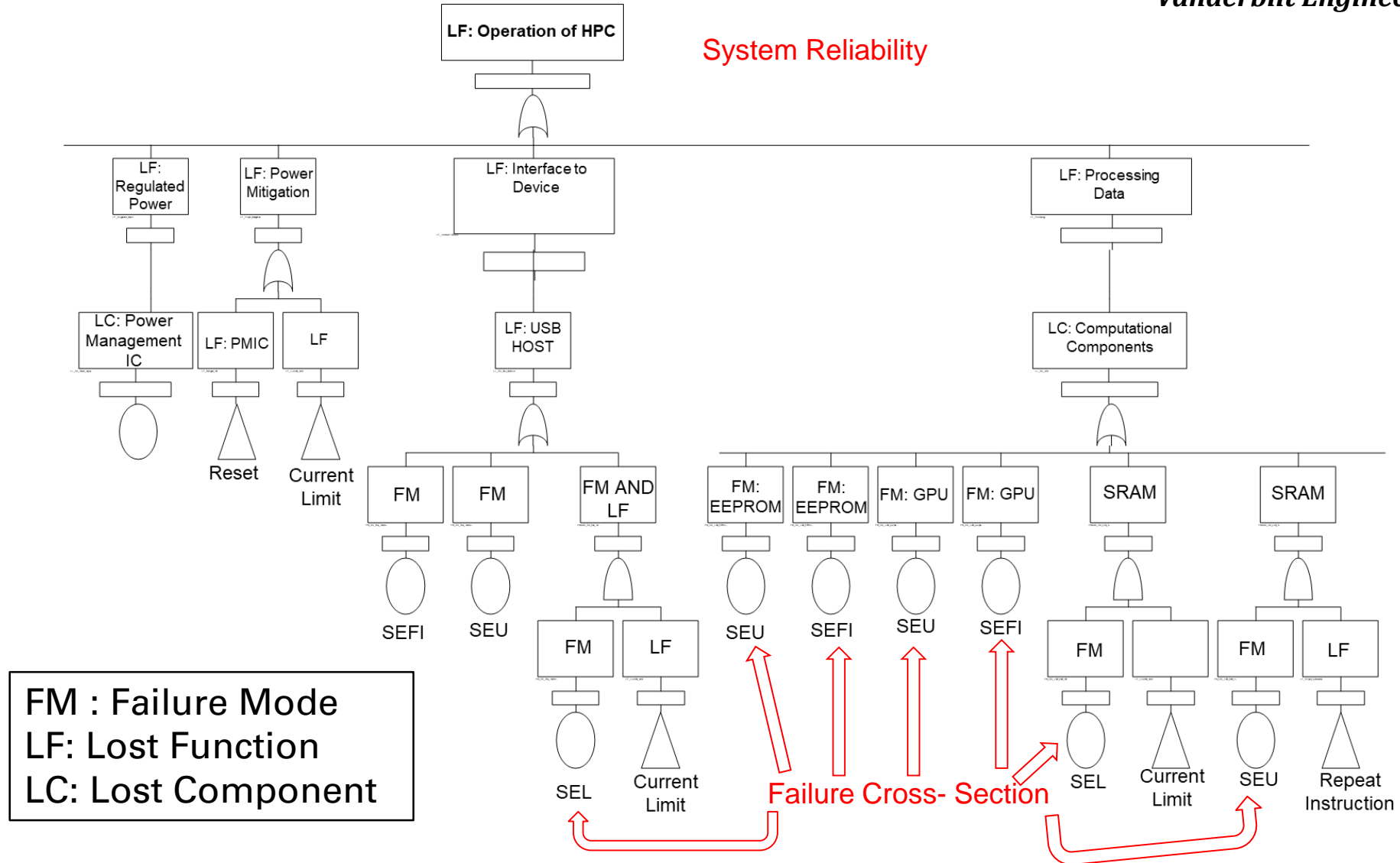
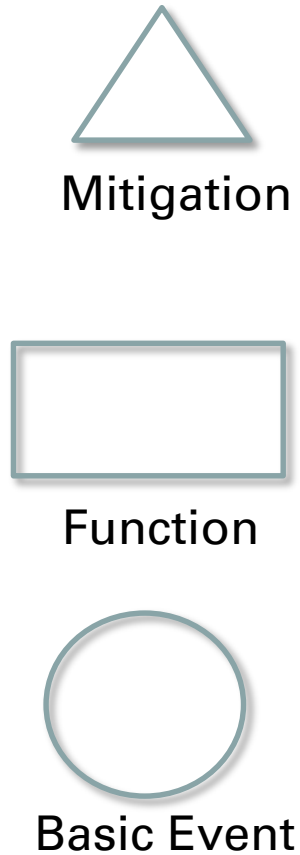


Each component block is cross-referenced to the system model where the faults are originated, propagated, and in some case, mitigated.

Fault Tree Analysis

- Based on Functional Decomposition

System Reliability



Summary



Developed the logical model to calculate system response of a BBB



Functional decomposition of a system/ architectural model enables failure analysis



Developed model is easy to update to changing rules or change in radiation cross-section of a component